

SHEET TITLE Revision 1.0

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	PCI SLOT ( NA )
17	ITE 8620 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC887-VD2
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX
27	VCORE ISL95812_1

SHEET TITLE


28	VCORE ISL95812_2
29	RT8120_DDR POWER
30	LPT
31	DVI
32	IT8892E ( NA )
33	USB3 VL805

## Circuit or PCB layout change

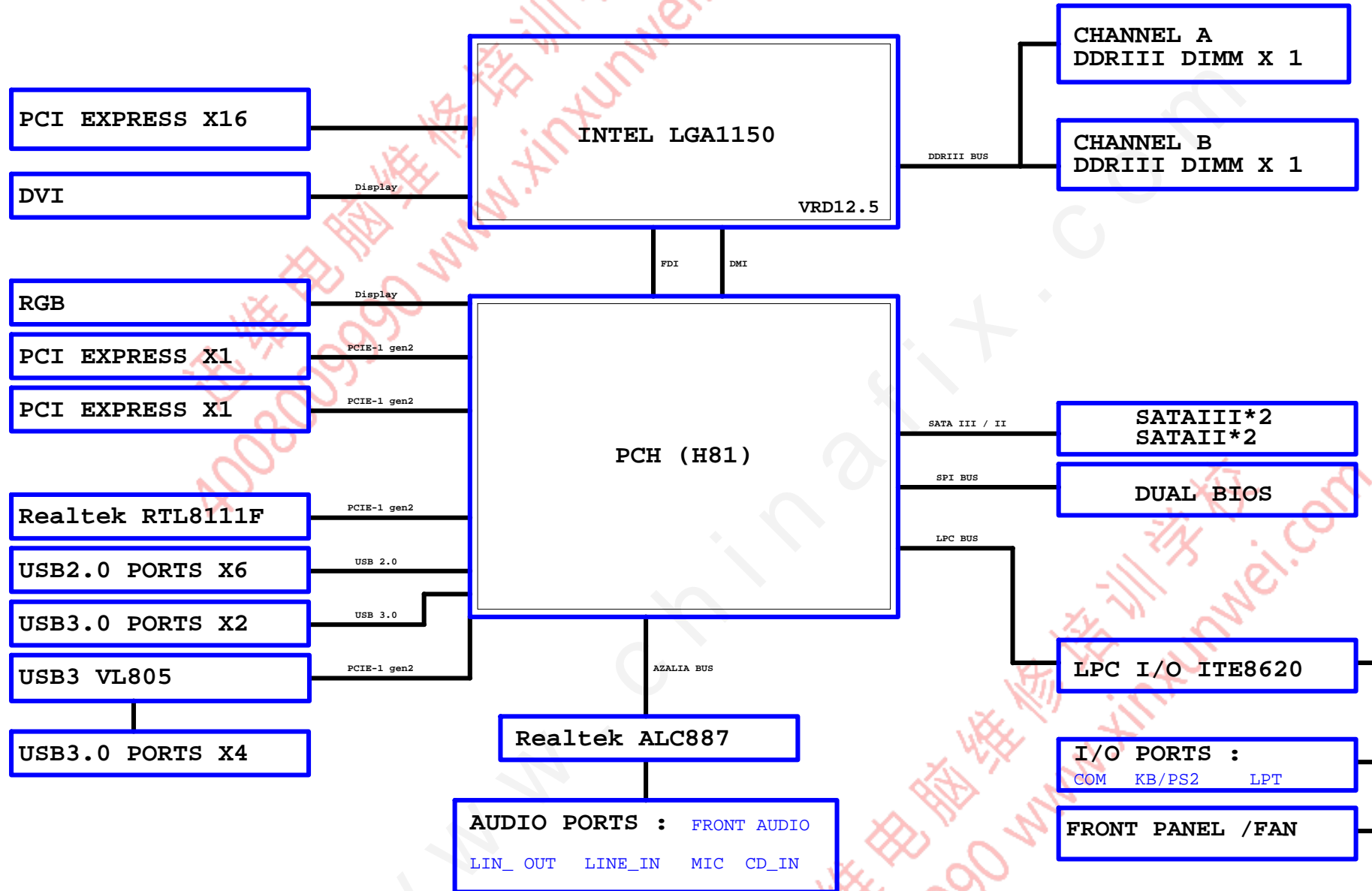
## 2013/04/08

[illegible]

S:單文  
4:四層板  
V:第二層是VCC  
N:咖啡色  
B:製程

<div style="text-align: center;">    <b>BOM &amp; PCB MODIFY HISTORY</b> </div>				
Title				Rev
Size	Document Number	<div style="text-align: center;"> <b>GA-H81M-D2W WG</b> </div>		<div style="text-align: center;"> <b>1.0</b> </div>
Custom				
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## BLOCK DIAGRAM







LGA1150

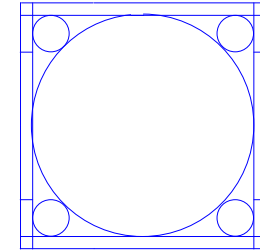
(A)

LGA1150

(B)

LGA1150

(CR)

CR  
CPU RETAINION/X

LGA1150



ILM\_BP/1156/CSP/ILM\_BP/1156/CSP/[12KRC-0F0001-52R\_12KRC-0F0001-51R]

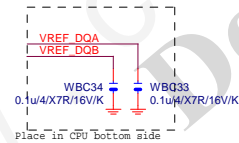
DDR BUS

MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5
MAAA6	AW17	DDR0_MA6	DDR0_D06	AE37	MDA6
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA8
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AM39	MDA21
AW8		DDR0_ODT2	DDR0_D18	AP38	MDA18
AW8		DDR0_ODT3	DDR0_D19	AP39	MDA19
AW33			DDR0_D20	AM37	MDA20
AW33			DDR0_D21	AM38	MDA16
AW33			DDR0_D22	AP47	MDA22
AW33			DDR0_D23	AP40	MDA23
AW33			DDR0_D24	AW37	MDA25
AW33			DDR0_D25	AW37	MDA29
AW33			DDR0_D26	AU35	MDA26
AW33			DDR0_D27	AU35	MDA27
AW33			DDR0_D28	AU37	MDA28
AW33			DDR0_D29	AU37	MDA24
AW33			DDR0_D30	AT35	MDA30
AW33			DDR0_D31	AW35	MDA31
AW33			DDR0_D32	AW35	MDA33
AW33			DDR0_D33	AU8	MDA37
AW33			DDR0_D34	AV4	MDA34
AW33			DDR0_D35	AW6	MDA35
AW33			DDR0_D36	AW6	MDA32
AW33			DDR0_D37	AW4	MDA38
AW33			DDR0_D38	AW4	MDA39
AW33			DDR0_D39	AR1	MDA41
AW33			DDR0_D40	AR4	MDA45
AW33			DDR0_D41	AN3	MDA42
AW33			DDR0_D42	AN4	MDA43
AW33			DDR0_D43	AR2	MDA44
AW33			DDR0_D44	AR3	MDA40
AW33			DDR0_D45	AN2	MDA46
AW33			DDR0_D46	AN1	MDA47
AW33			DDR0_D47	AL1	MDA49
AW33			DDR0_D48	AL4	MDA53
AW33			DDR0_D49	AL4	MDA50
AW33			DDR0_D50	AJ4	MDA51
AW33			DDR0_D51	AL2	MDA52
AW33			DDR0_D52	AL3	MDA48
AW33			DDR0_D53	AJ2	MDA54
AW33			DDR0_D54	AJ1	MDA55
AW33			DDR0_D55	AG1	MDA57
AW33			DDR0_D56	AG4	MDA61
AW33			DDR0_D57	AE3	MDA58
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AW33			DDR0_D59	AG2	MDA60
AW33			DDR0_D60	AG3	MDA56
AW33			DDR0_D61	AE2	MDA62
AW33			DDR0_D62	AE1	MDA63
AW33			DDR0_D63	AE39	DQSA0
AW33			DDR0_D64	AJ39	DQSA1
AW33			DDR0_D65	AN39	DQSA2
AW33			DDR0_D66	AV36	DQSA3
AW33			DDR0_D67	AV5	DQSA4
AW33			DDR0_D68	AP3	DQSA5
AW33			DDR0_D69	AK3	DQSA6
AW33			DDR0_D70	AF3	DQSA7
AW33			DDR0_D71	AV32	DQSA0
AW33			DDR0_D72	AE38	DQSA1
AW33			DDR0_D73	AJ38	DQSA2
AW33			DDR0_D74	AN38	DQSA3
AW33			DDR0_D75	AJ36	DQSA4
AW33			DDR0_D76	AW5	DQSA5
AW33			DDR0_D77	AP2	DQSA6
AW33			DDR0_D78	AK2	DQSA7
AW33			DDR0_D79	AF2	DQSA7
AW33			DDR0_D80	AJ32	

HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

MAAB0	AL19	DDR1_MA0	AE34	MD80
MAAB1	AK23	DDR1_MA1	AE35	MD81
MAAB2	AM22	DDR1_MA2	AG35	MD82
MAAB3	AM23	DDR1_MA3	AH35	MD83
MAAB4	AP23	DDR1_MA4	AD34	MD84
MAAB5	AL23	DDR1_MA5	AD35	MD85
MAAB6	AY24	DDR1_MA6	AG34	MD86
MAAB7	AV25	DDR1_MA7	AH34	MD87
MAAB8	AU26	DDR1_MA8	AL34	MD88
MAAB9	AW25	DDR1_MA9	AL35	MD89
MAAB10	AP18	DDR1_MA10	AL31	MD810
MAAB11	AK38	DDR1_MA11	AL31	MD811
MAAB12	AV28	DDR1_MA12	AK34	MD812
MAAB13	AR15	DDR1_MA13	AK35	MD813
MAAB14	AV27	DDR1_MA14	AK32	MD814
MAAB15	AV28	DDR1_MA15	AL32	MD815
MODT_B0	AM17	DDR1_ODT0	AP34	MD817
MODT_B1	AL16	DDR1_ODT1	AN31	MD819
AM16		DDR1_ODT2	AP31	MD823
AK15		DDR1_ODT3	AP35	MD820
AM26		DDR1_ECC0	AP35	MD816
AM25		DDR1_ECC1	AN32	MD818
AP25		DDR1_ECC2	AP32	MD822
AP28		DDR1_ECC3	AM29	MD825
AL26		DDR1_ECC4	AM28	MD828
AL25		DDR1_ECC5	AR29	MD827
AR26		DDR1_ECC6	AR28	MD830
AR26		DDR1_ECC7	AL23	MD834
AR26			AL28	MD829
AR26			AP29	MD826
AR26			AP28	MD831
AR26			AP12	MD832
AR26			AP12	MD833
AR26			AP13	MD834
AR26			AL12	MD835
AR26			AR13	MD836
AR26			AP13	MD837
AR26			AM13	MD838
AR26			AM12	MD839
AR26			AR9	MD845
AR26			AP9	MD841
AR26			AR6	MD847
AR26			AP6	MD843
AR26			AR10	MD844
AR26			AP10	MD840
AR26			AP7	MD842
AR26			AM9	MD852
AR26			AL9	MD853
AR26			AL6	MD850
AR26			AL7	MD855
AR26			AM10	MD848
AR26			AL10	MD849
AR26			AM6	MD854
AR26			AM2	MD851
AR26			AH6	MD861
AR26			AH7	MD860
AR26			AE6	MD859
AR26			AE7	MD863
AR26			AJ6	MD856
AR26			AJ7	MD857
AR26			AF6	MD858
AR26			AF7	MD862
AR26			AF35	DQSB0
AR26			AL33	DQSB1
AR26			AP33	DQSB2
AR26			AN28	DQSB3
AR26			AN12	DQSB4
AR26			AP8	DQSB5
AR26			AL8	DQSB6
AR26			AG7	DQSB7
AR26			AN25	
AR26			AK33	DQSB0
AR26			AN33	DQSB2
AR26			AN29	DQSB3
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AR26			AM8	DQSB6
AR26			AG6	DQSB7
AR26			AN28	

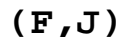
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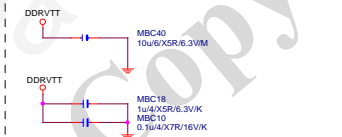
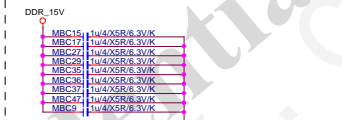
Place in CPU bottom side

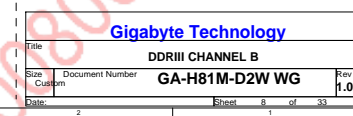
Gigabyte Technology

Title			CPU LGA1150-B		
Size			GA-H81M-D2W WG		
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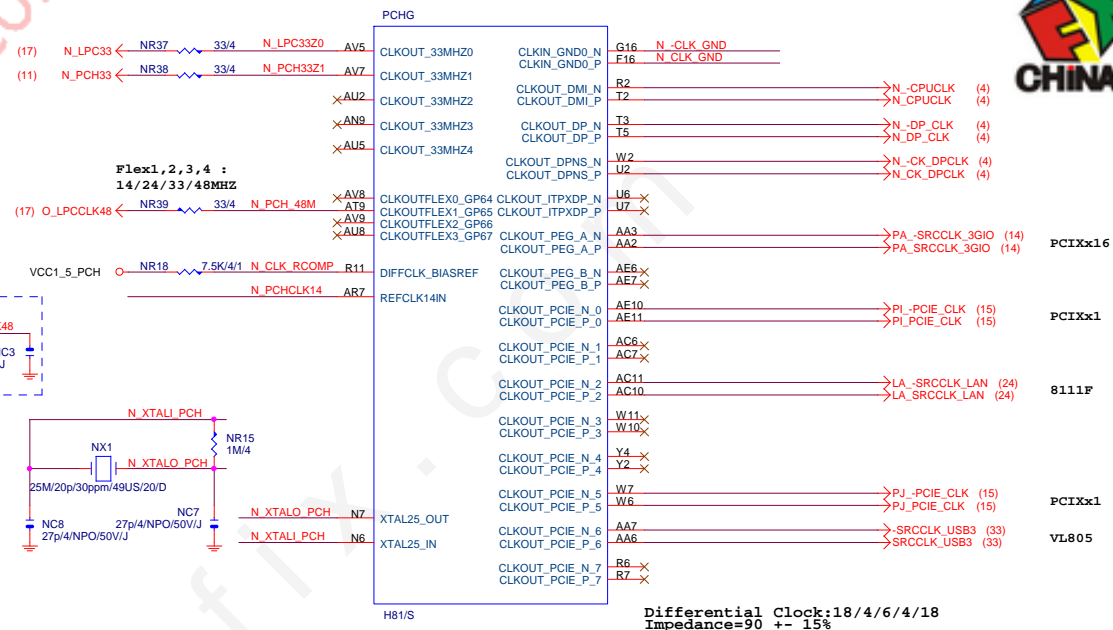
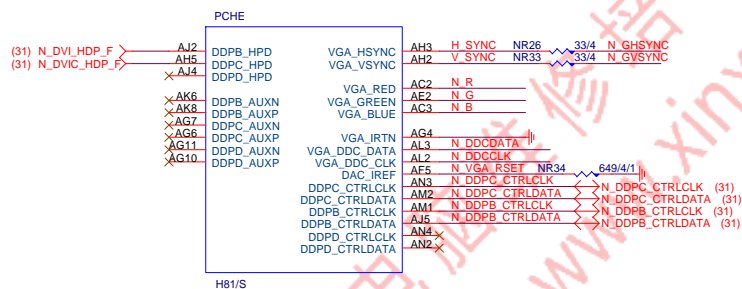
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CPU LGA1150-C			
Size	Document Number		Rev
Custom	GA-H81M-D2W WG		1.0
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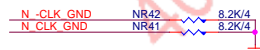






Differential Clock:18/4/6/4/18  
Impedance=90 +- 15%

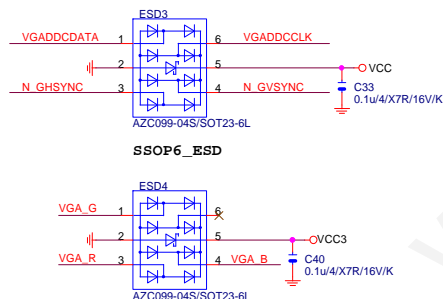
PCH CLK PD



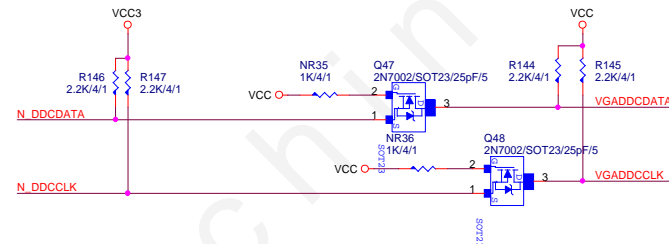
Mount for integrated clock Generation  
Mode



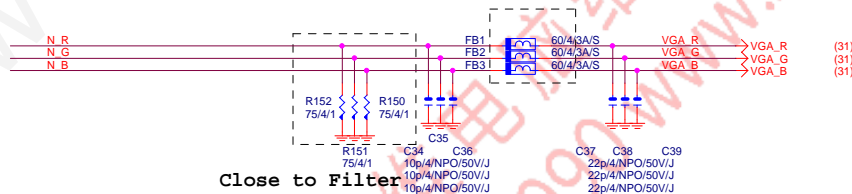
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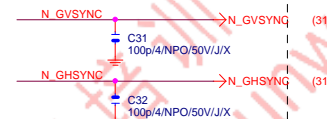
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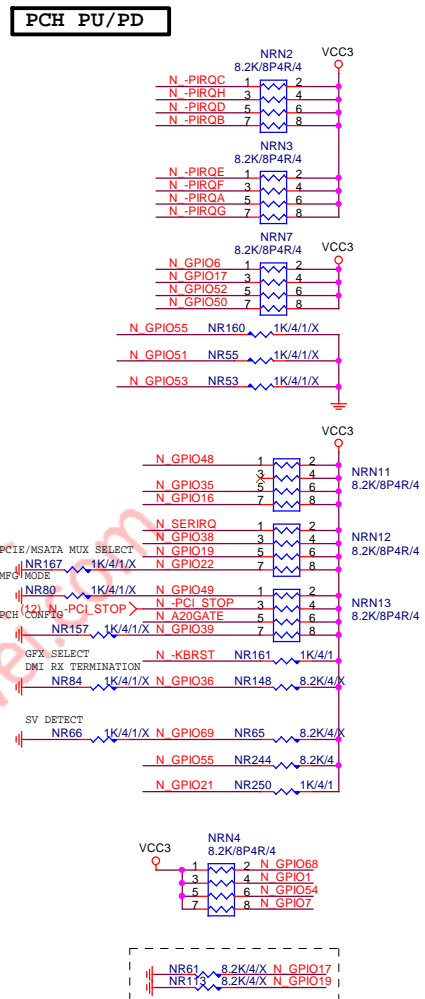
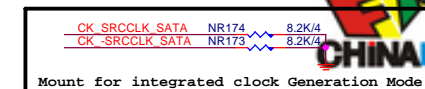
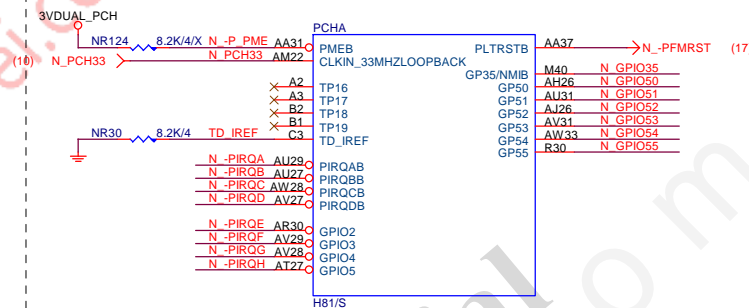


## VGA DDC

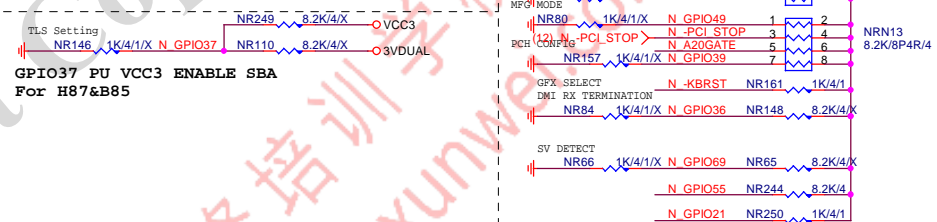


## VGA CONNECTOR

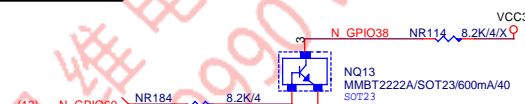




ME PWROK



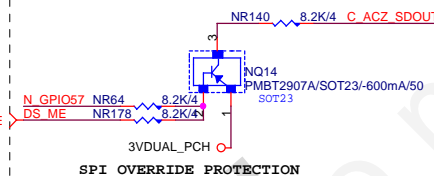
GPI038 Ctrl



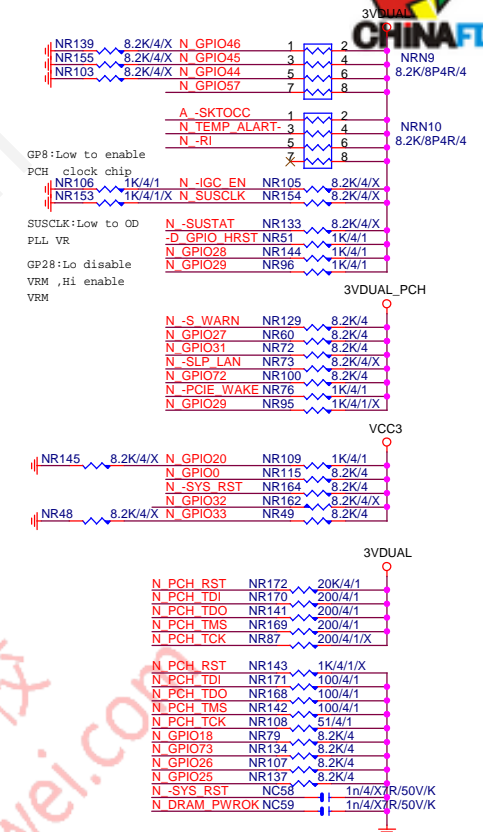
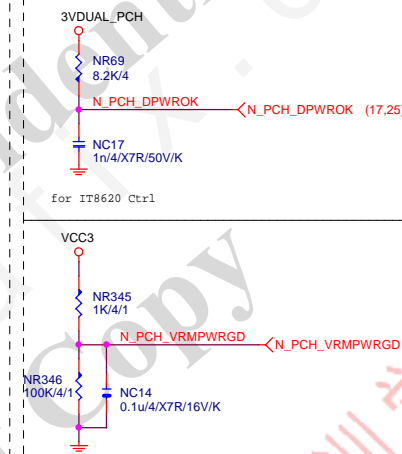


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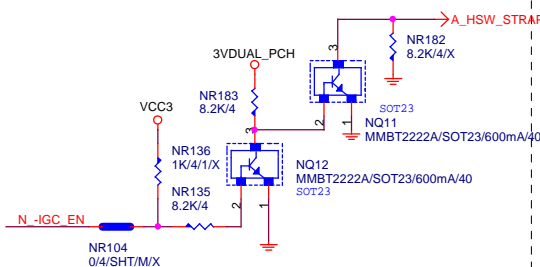
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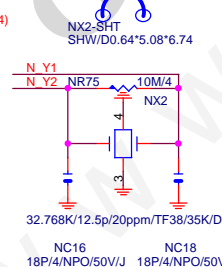
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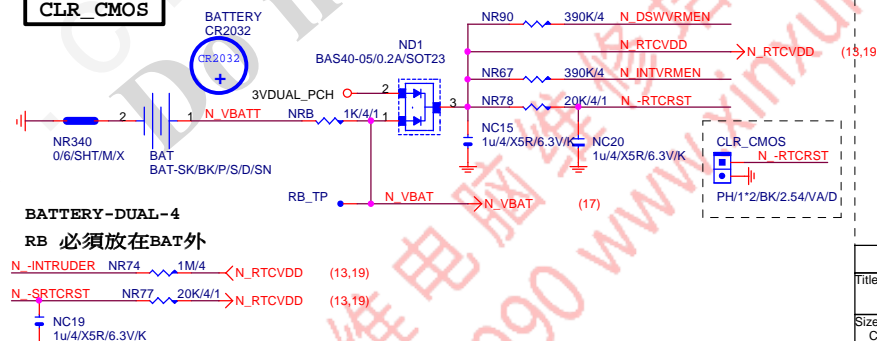
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32.768KHZ



CLR_CMOS
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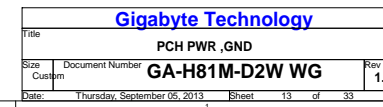
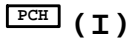


## Gigabyte Technology

Title			
PCH GPIO , CTRL , AUDIO			
Size	Document Number	Rev	
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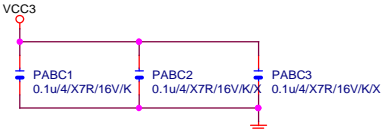


SHT PWR

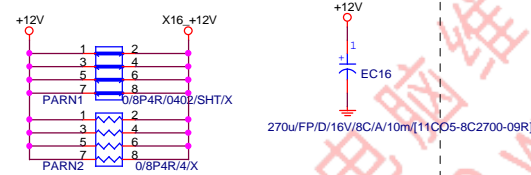




## PCIEX16 CAP



## PCIEX16 PROTECT SHT



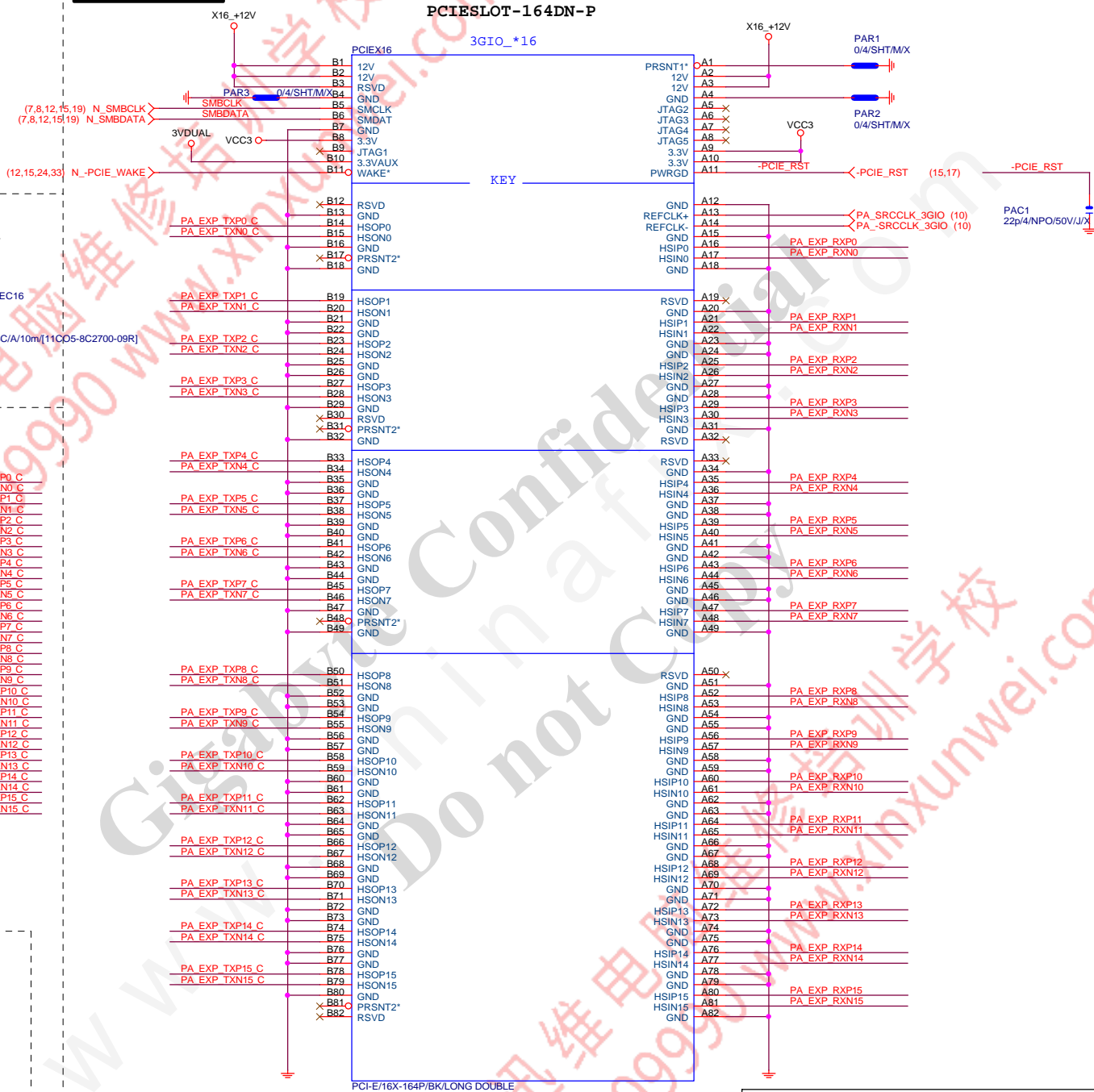
## PCIEX16 AC CAP

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PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXP0.15] >>> PA\_EXP\_RXP[0.15] (4)  
PA EXP RXN0.15] >>> PA\_EXP\_RXN[0.15] (4)  
PA EXP TXP0.15] >>> PA\_EXP\_TXP[0.15] (4)  
PA EXP TXN0.15] >>> PA\_EXP\_TXN[0.15] (4)

The auxiliary reset circuit is only required for PCIe Gen3 margining and functional link training

## PCIEX16 SLOT

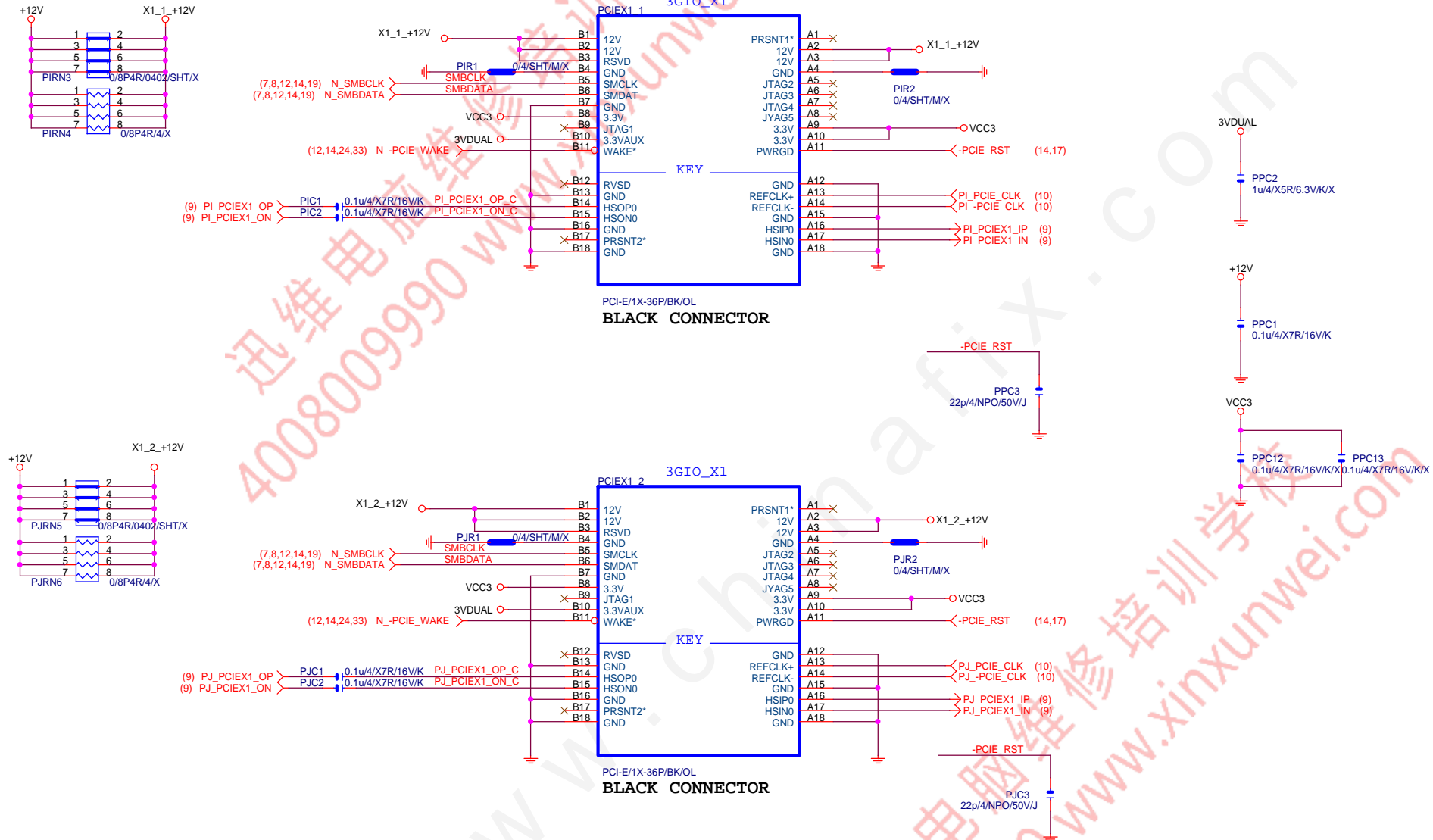


BLACK CONNECTOR

Gigabyte Technology

Title			PCI EXPRESS * 16		
Size			GA-H81M-D2W WG		
Custom			Rev 1.0		
Date:			Thursday, September 05, 2013		
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## PCIEX1 SLOT



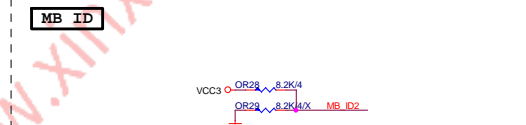
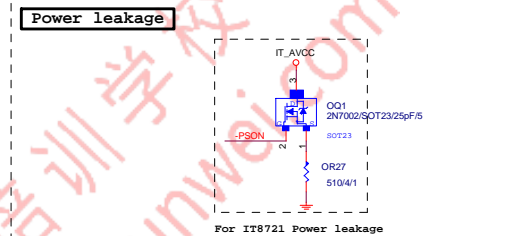
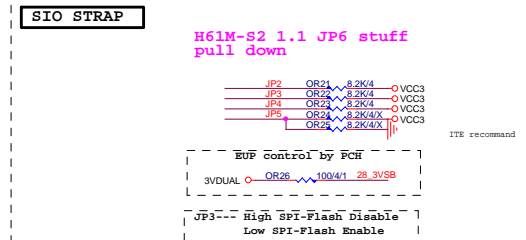
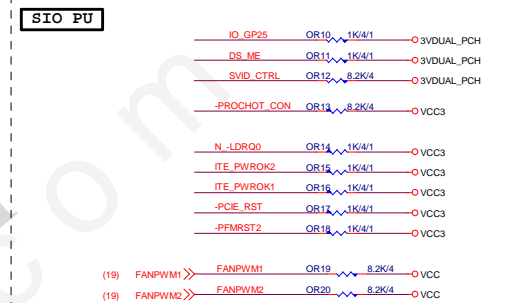
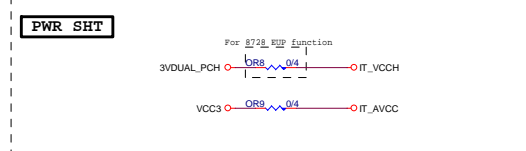
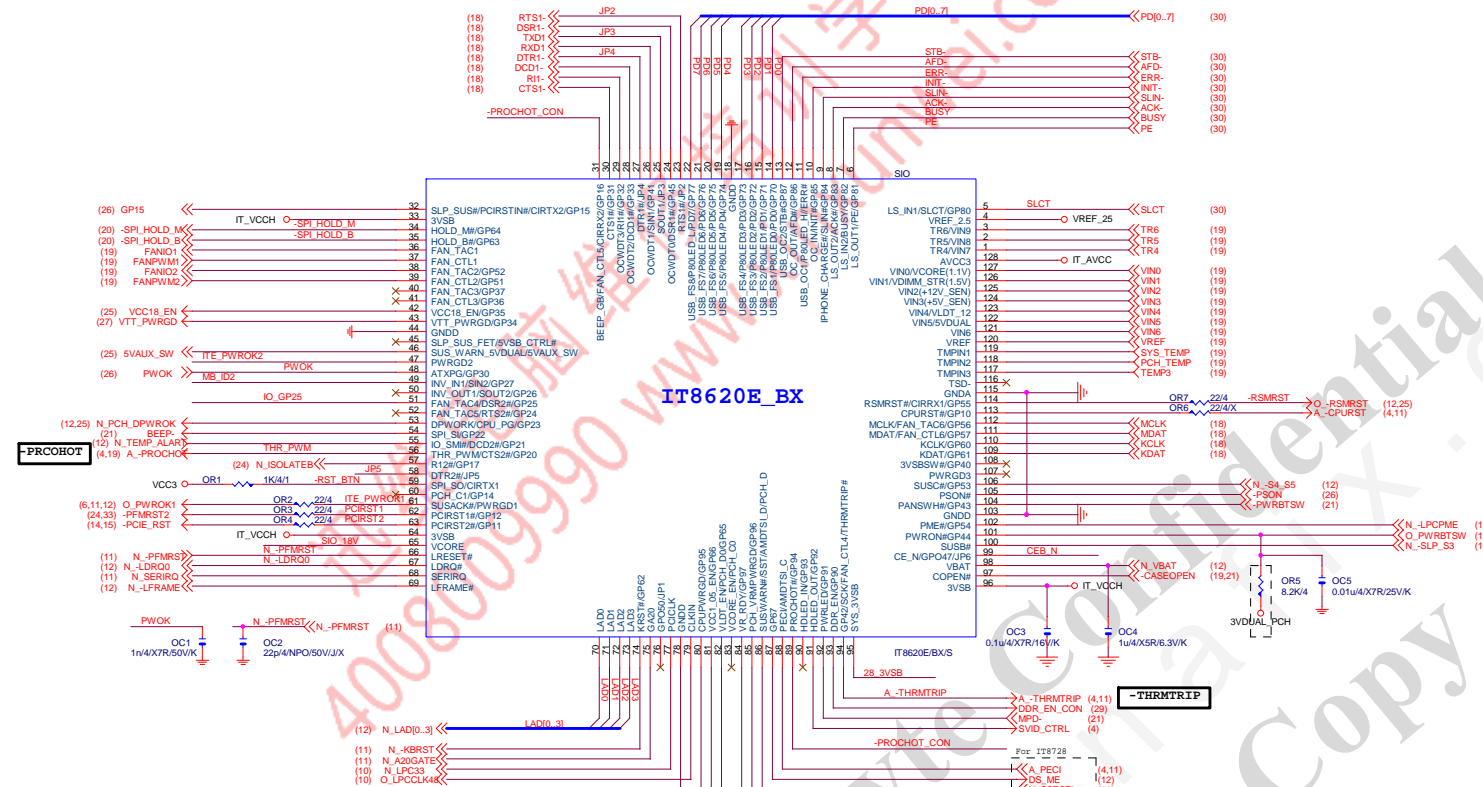
## Gigabyte Technology

Title		
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Size	Document Number	Rev
Custom	GA-H81M-D2W WG	1.0
Date:	Thursday, September 05, 2013	Sheet 15 of 33

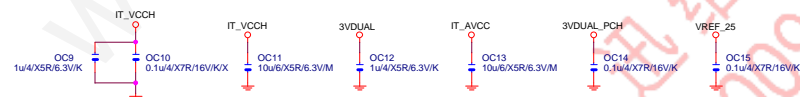


Gigabyte Technology		
Title		
PCI SLOT 1&2		
Size	Document Number	Rev
Custom	GA-H81M-D2W WG	1.0
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	2	1

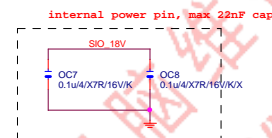




SIO CAP

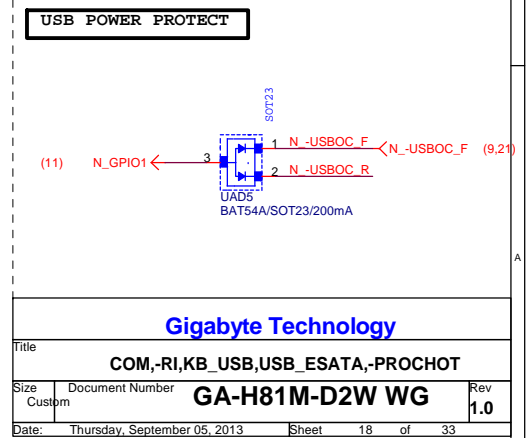
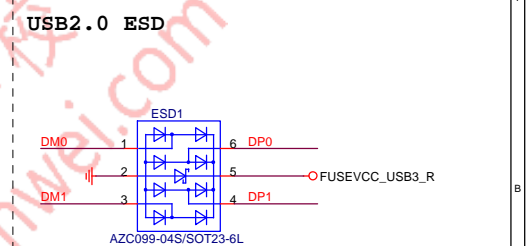
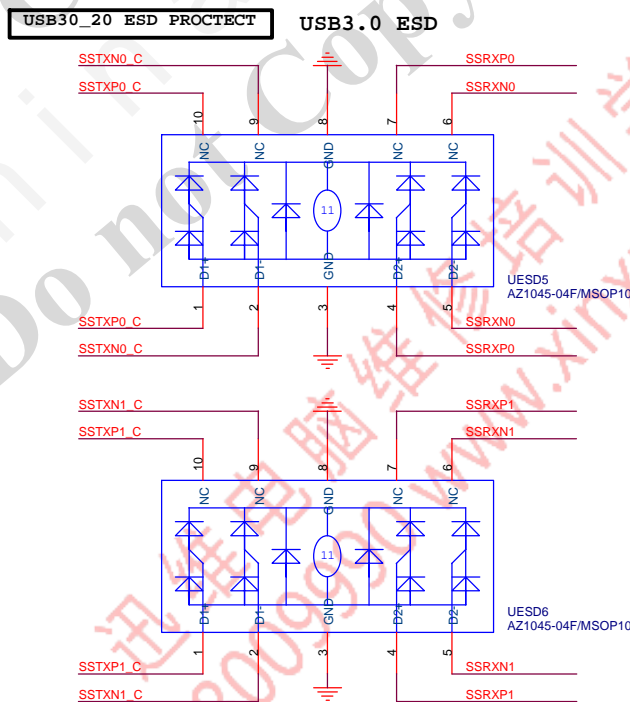
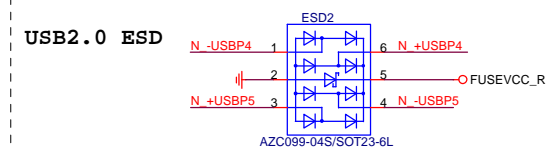
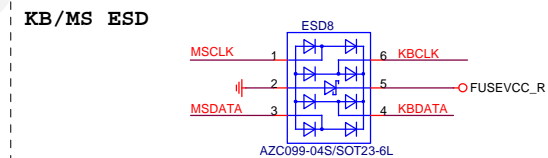
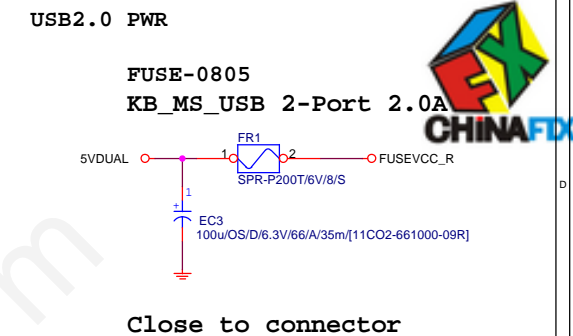
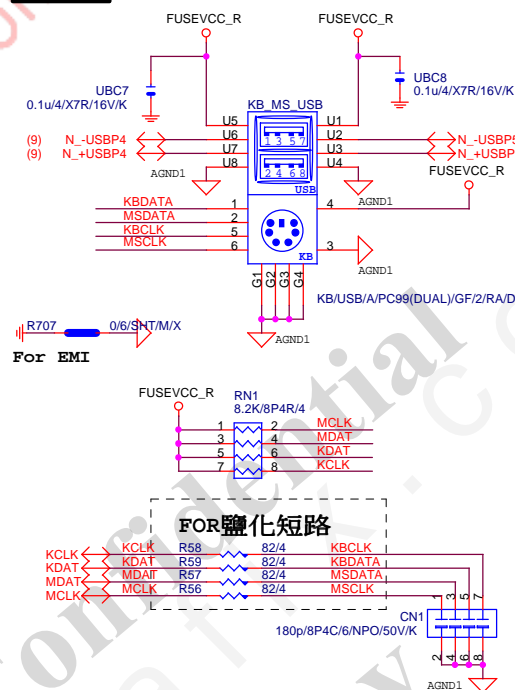
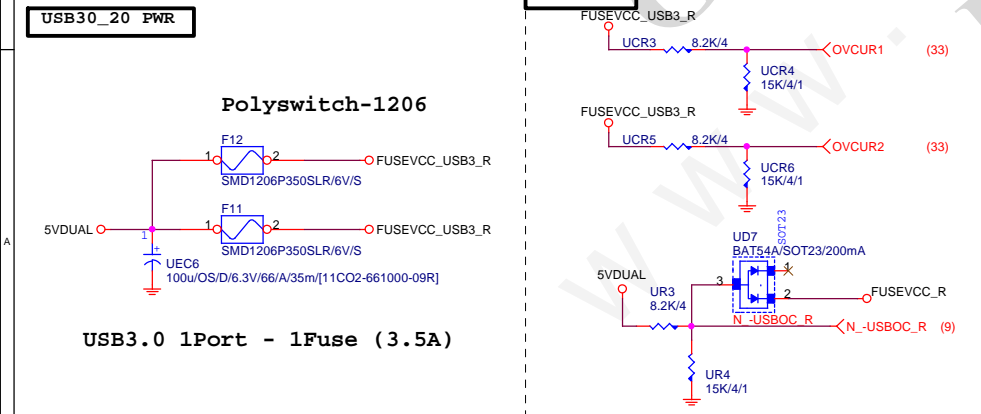


SIO\_18V

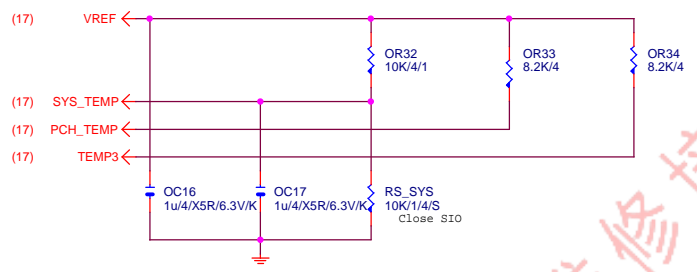


MB ID

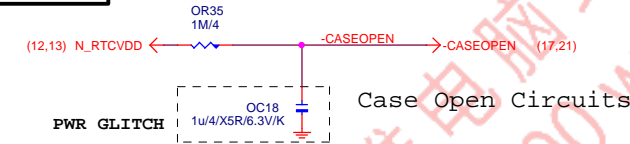




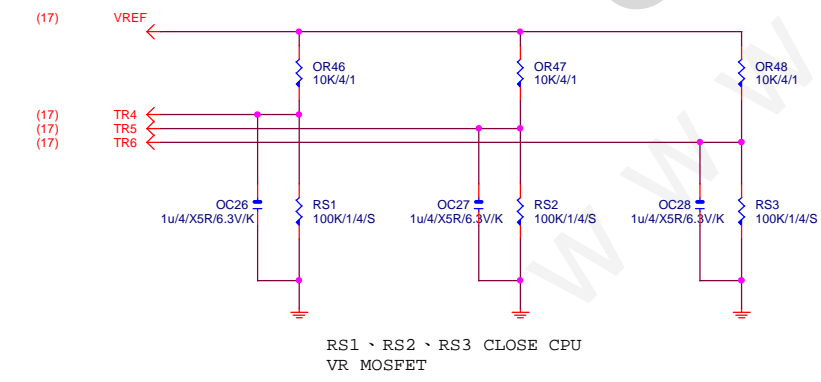
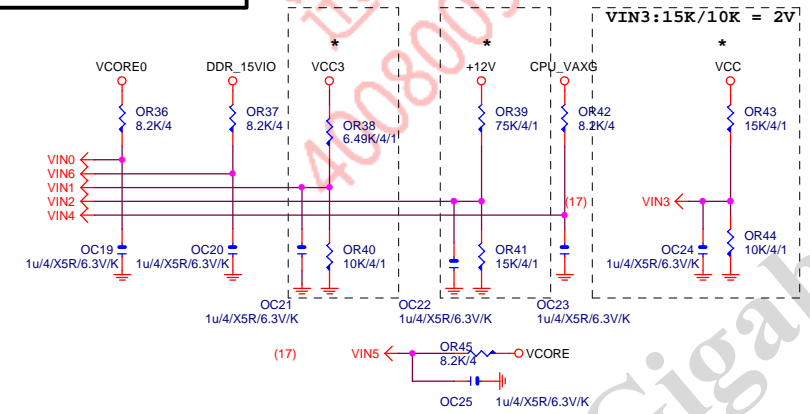
TEMP H/W MONITOR



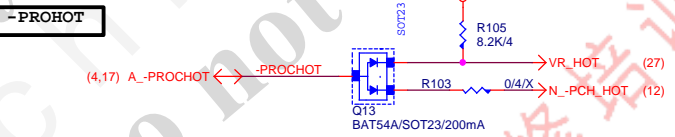
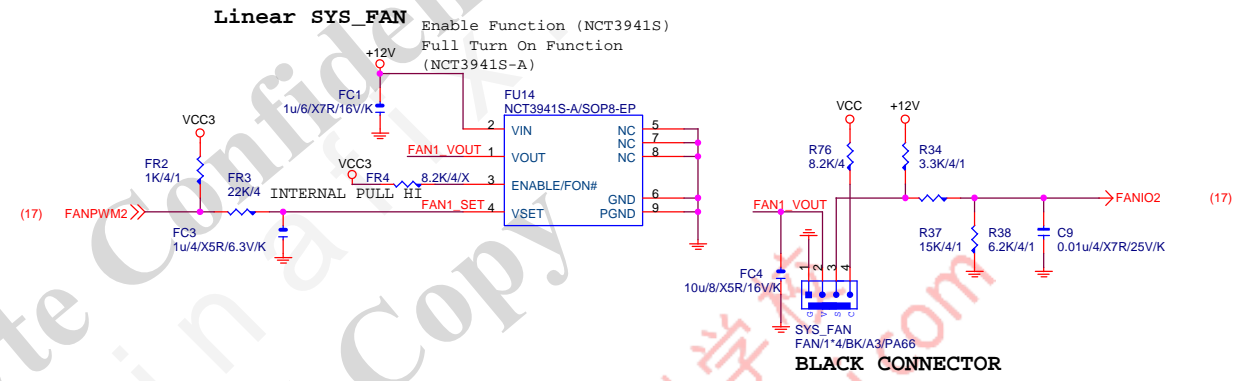
CASE OPEN



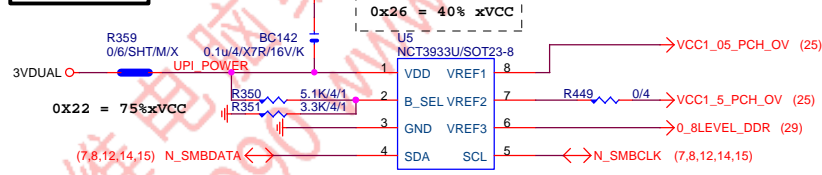
VOLTAGE-- H/W MONITOR



SYS SMART FAN

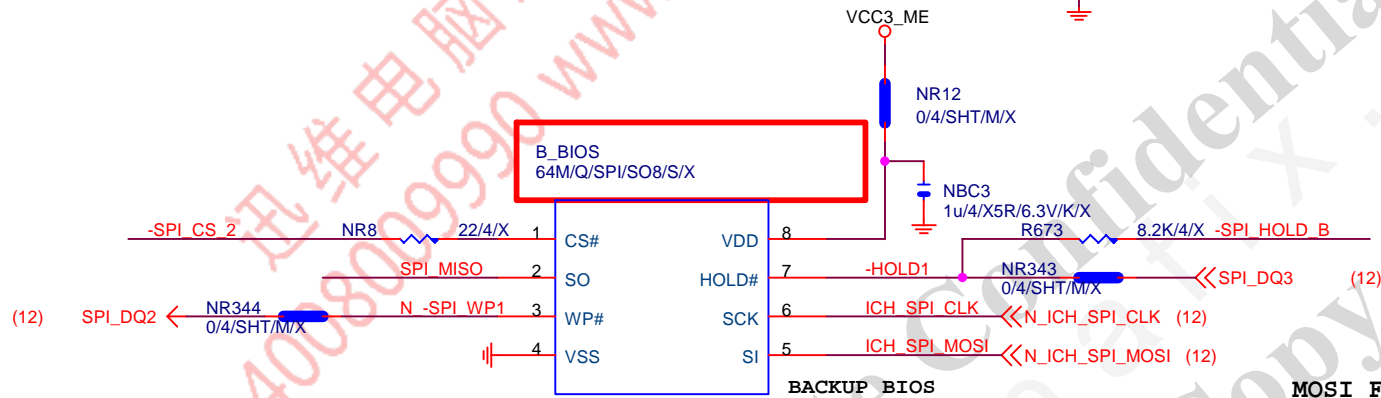
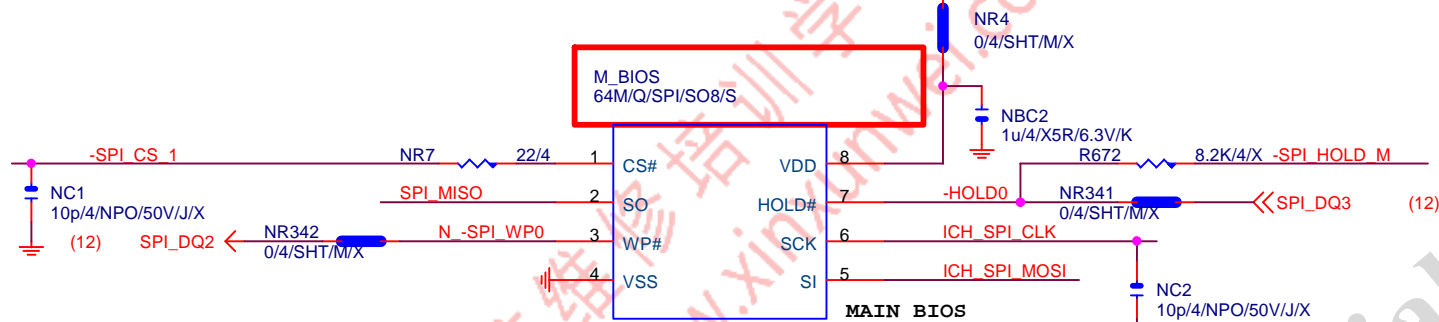


OV NCT3933



NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

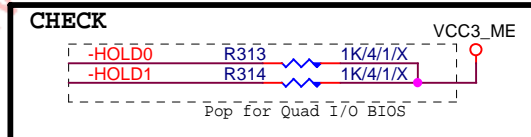
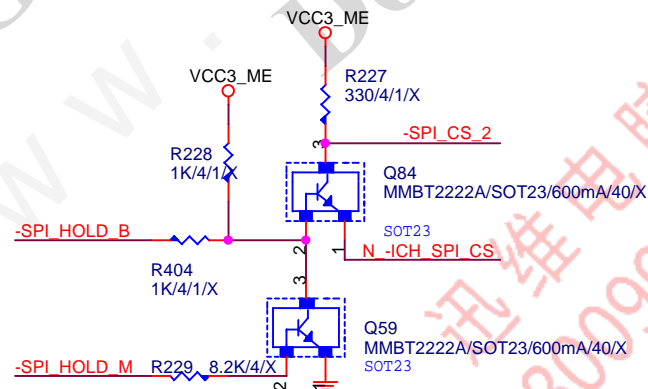
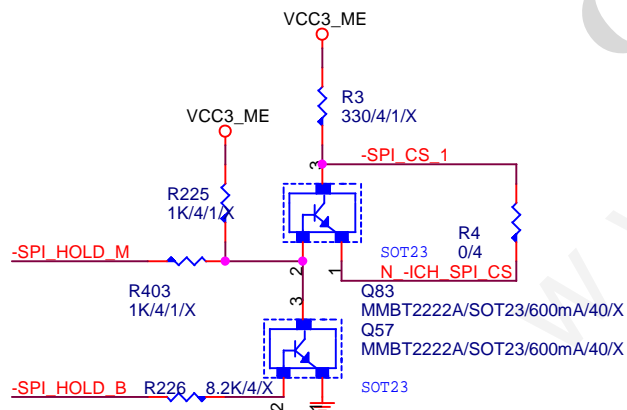
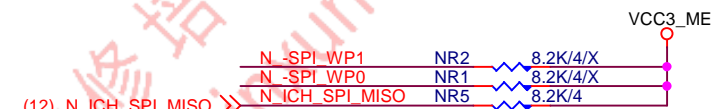
Gigabyte Technology			
Title HWM,FAN CTRL,OV			
Size Custom	Document Number	GA-H81M-D2W WG	
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BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating  
0 means PD 1K

#### MOSI For DMI RX Termination Voltage

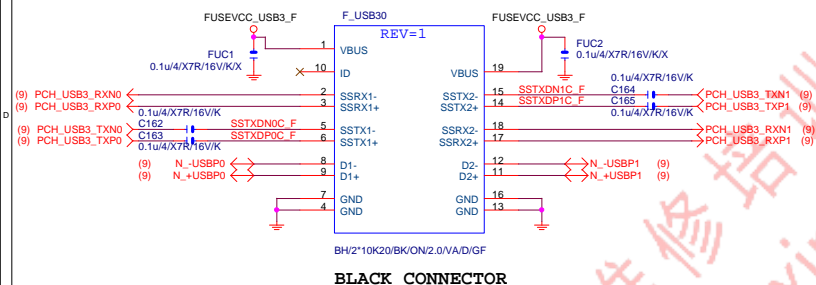
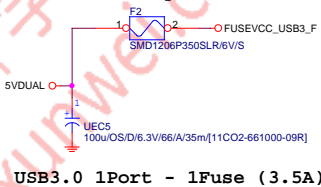
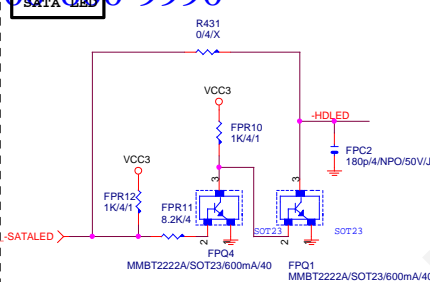
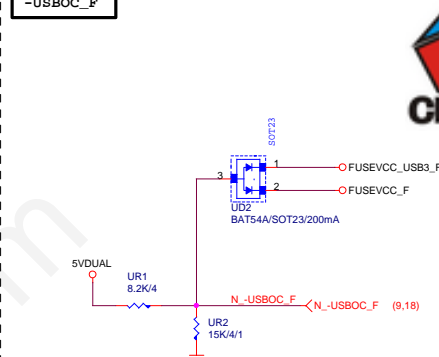
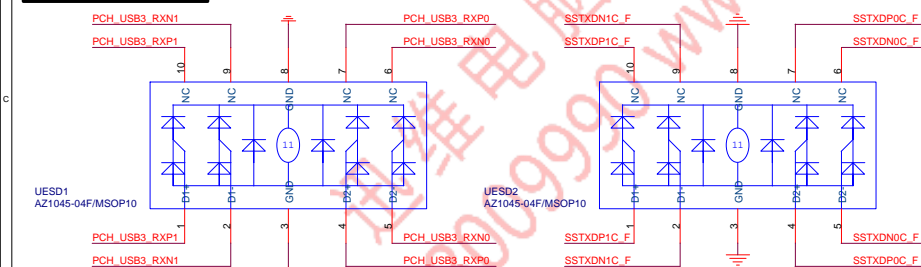
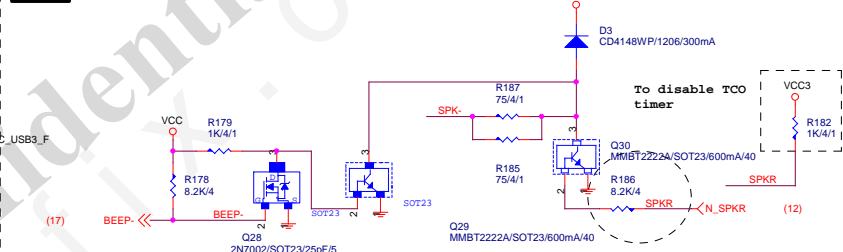
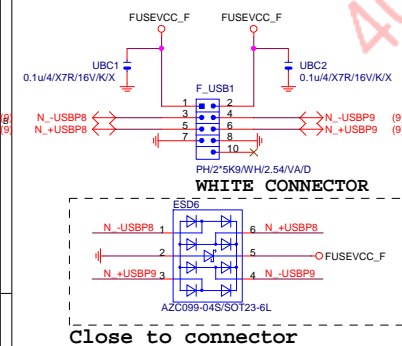
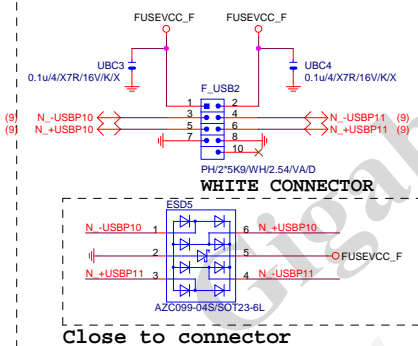
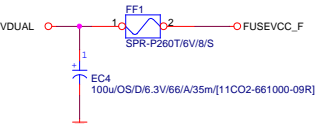
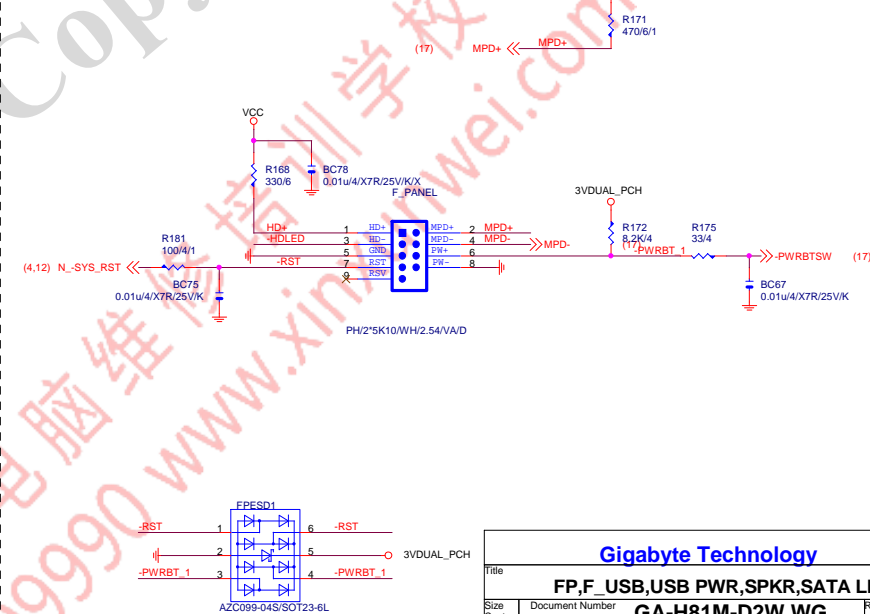


**Gigabyte Technology**

#### DUAL BIOS

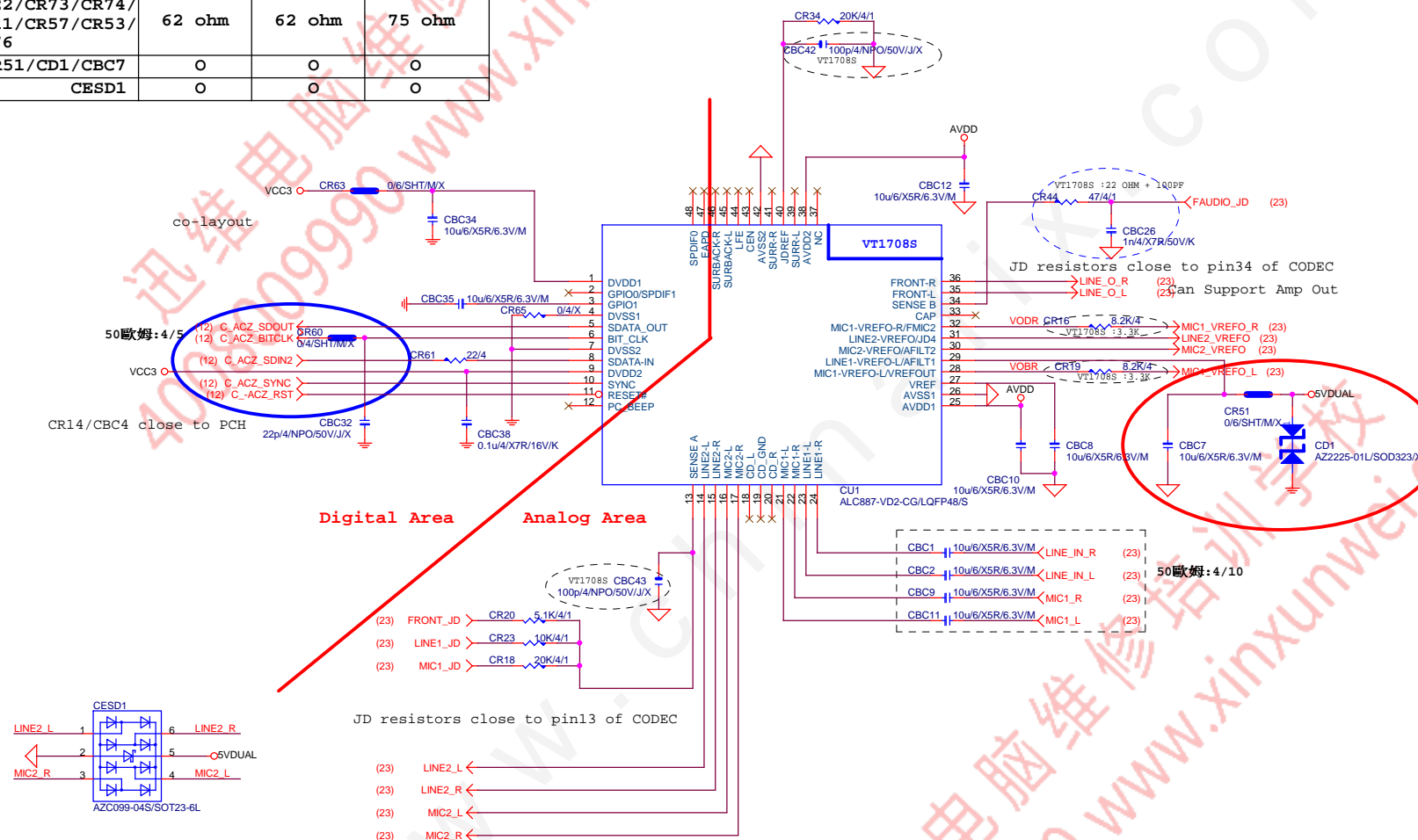
Title	GA-H81M-D2W WG		Rev 1.0
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**F\_USB30****Polyswitch-1206****SATA\_LED****-USB0C\_F****F\_USB30 ESD PROTECT****SPKR****FRONT USB1****FRONT USB2**
**FUSE-0805**  
**F\_USB1, F\_USB2 4-Port 2.6A**
**INTEL FRONT PANEL****Gigabyte Technology**

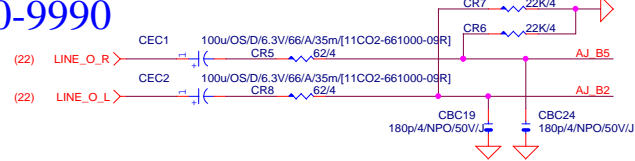
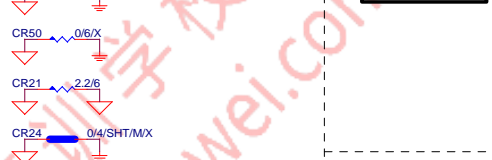
FP, F_USB, USB PWR, SPKR, SATA LED			
Size	Document Number	GA-H81M-D2W WG	
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	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O





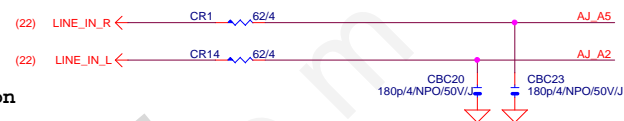
www.xinxunwei.com 400-800-9990



### LINE-IN

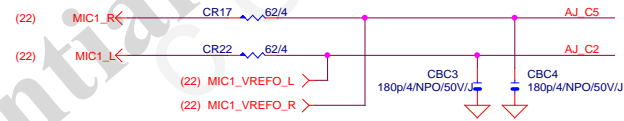
Verify MIC function  
in LINE-in

Only reserved for ALC888

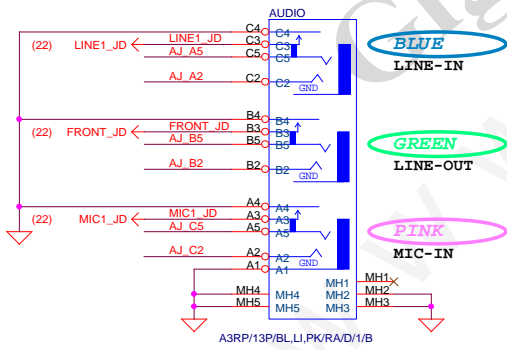


For 889A/888

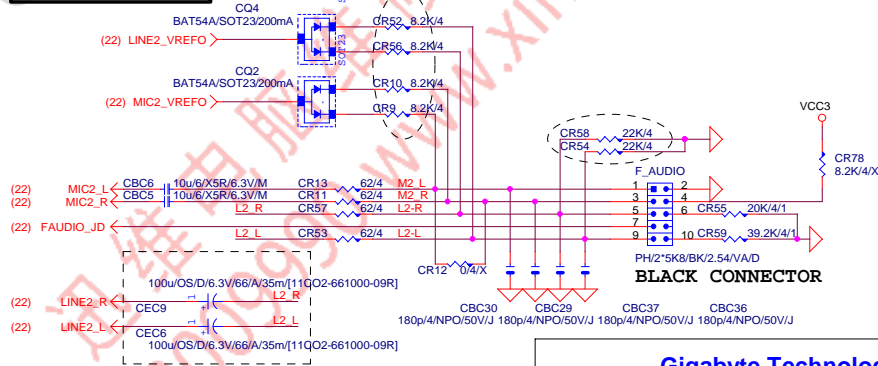
### MIC-IN



### SPDIF\_OUT



### AZALIA FRONT PANEL

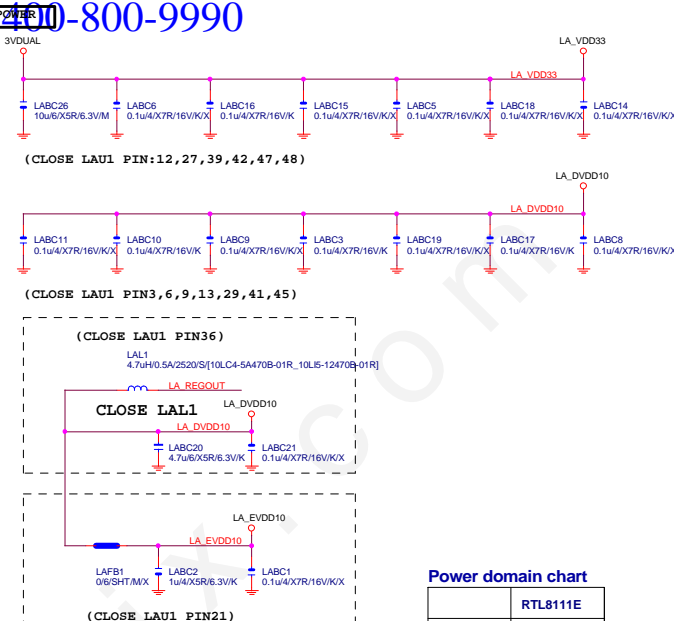
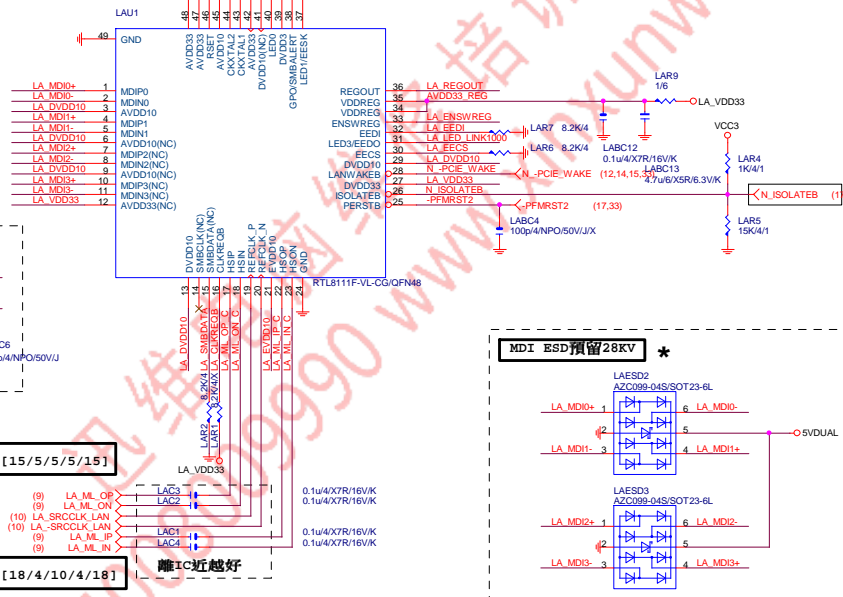


Gigabyte Technology

Title			
AUDIO JACK			
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LAN:RTL8111F/VB/VL

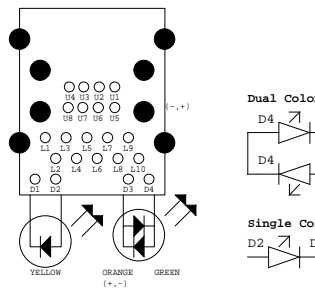
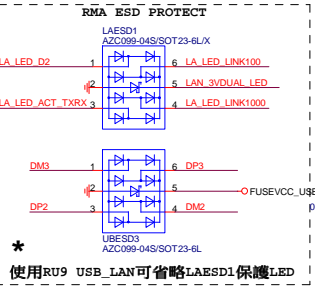
www.xinxunwei.com 400-800-9990



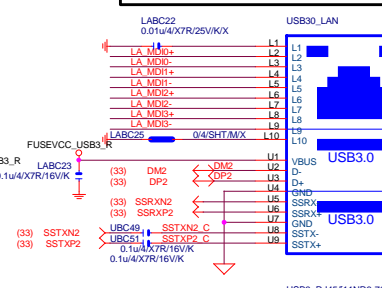
**Power domain chart**

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

**USB LAN CONNECTOR**



**LA\_MDI-->100歐姆:[20/4/8/4/20]**



**注意:USB PORT(目前:暫代6,7PORT)**  
**USB-->90歐姆:[15/4.5/7.5/4.5/15]**

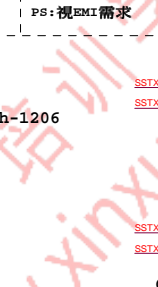
**BOM NOTICE**

料號	規格	廠商
11NR6-702009-96R 1G LAN (12core)	規格	廠商
[LED獨立走線,可省略外加AZC099料件LAESD1]		
1. 9KV ESD BOM:		
USB LAN (RU9):11NR6-702009-96R		
2. 28KV ESD BOM:		
USB LAN (RU9):11NR6-702009-96R		
LAESD2,LAESD3:上件AZC398-04S		

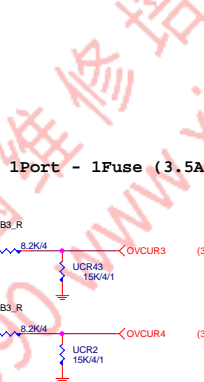
**USB X3 POWER**



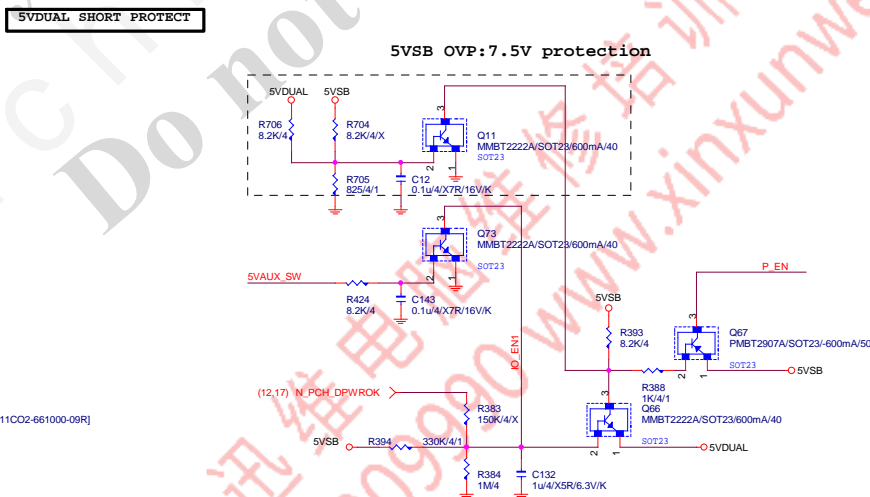
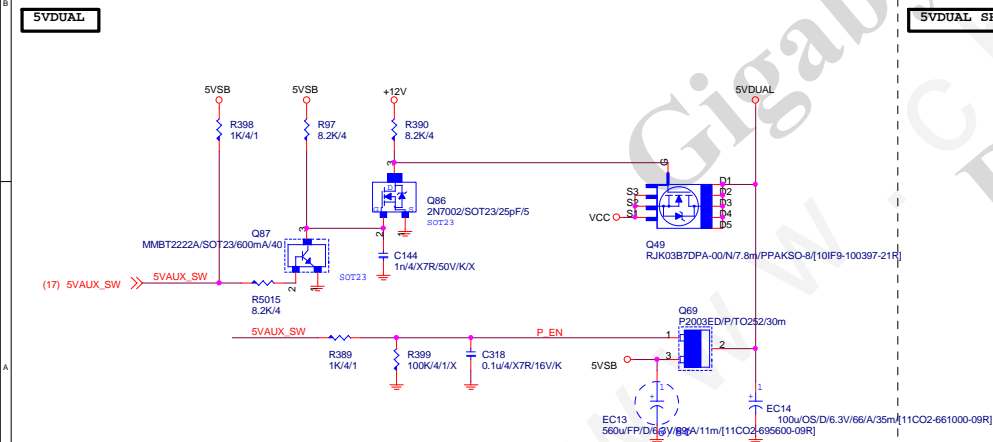
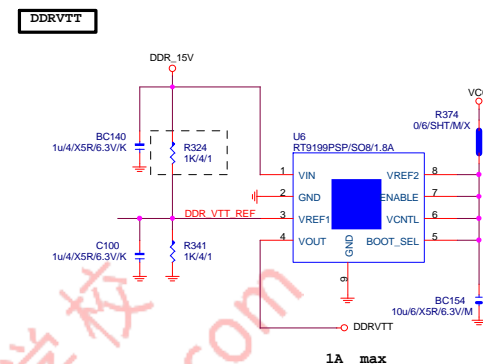
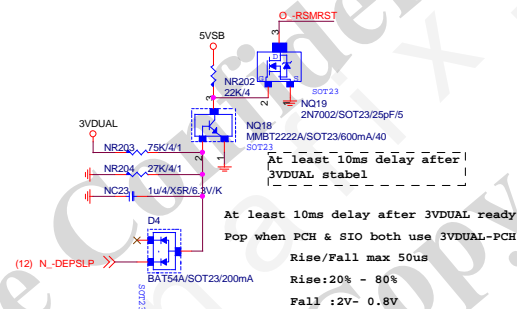
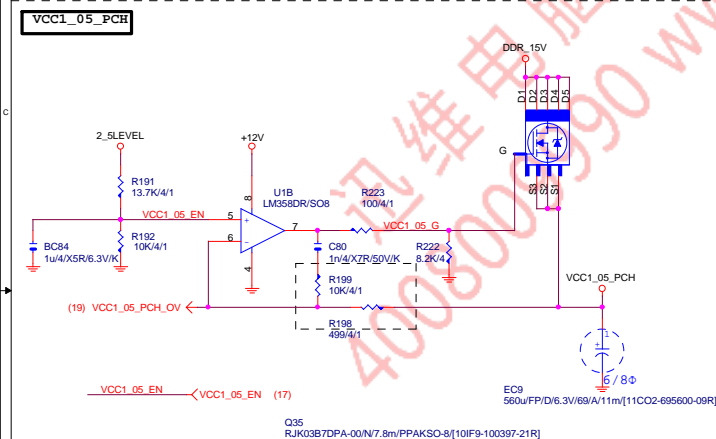
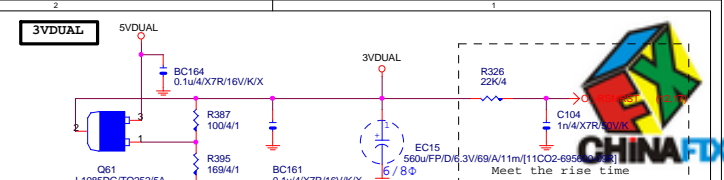
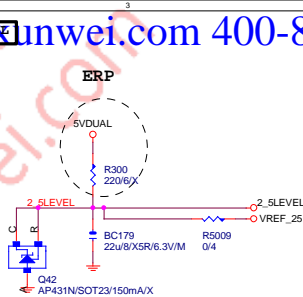
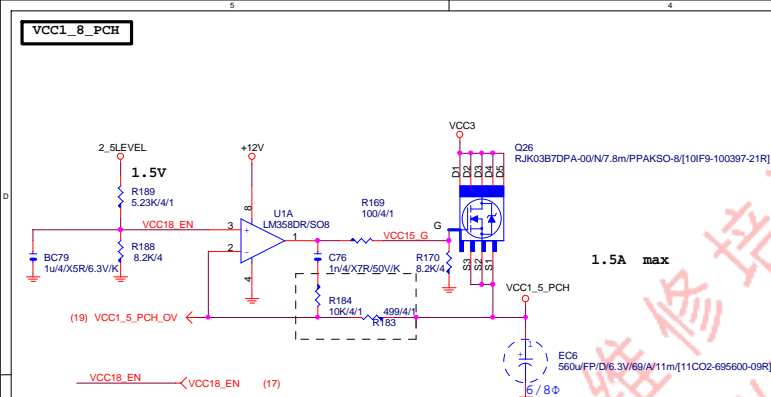
**EMI SHORT PAD**



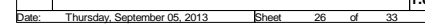
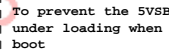
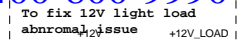
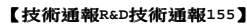
**Polyswitch-1206**

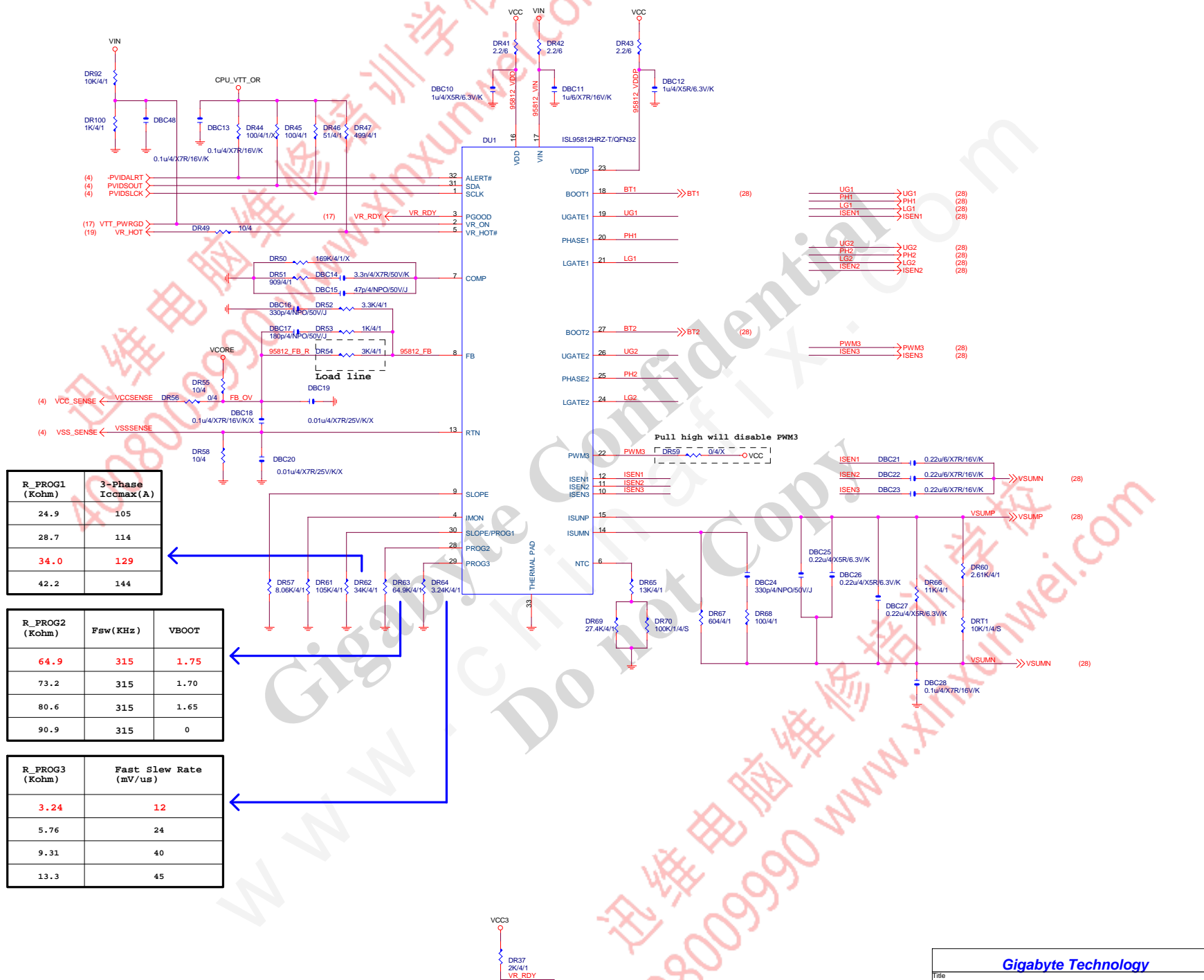




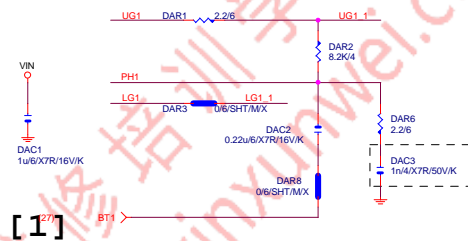


Gigabyte Technology			
Title			
DISCRETE POWER			
Size	Document Number	GA-H81M-D2W WG	Rev
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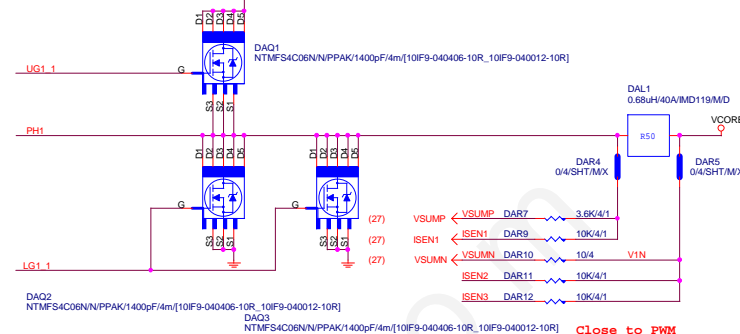




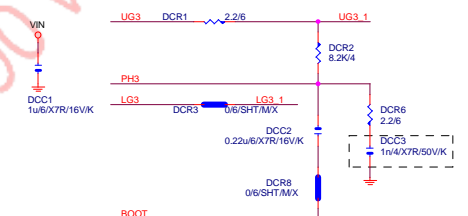
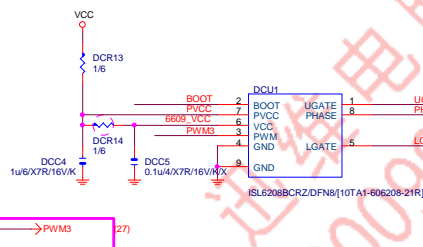
# PHASE 1



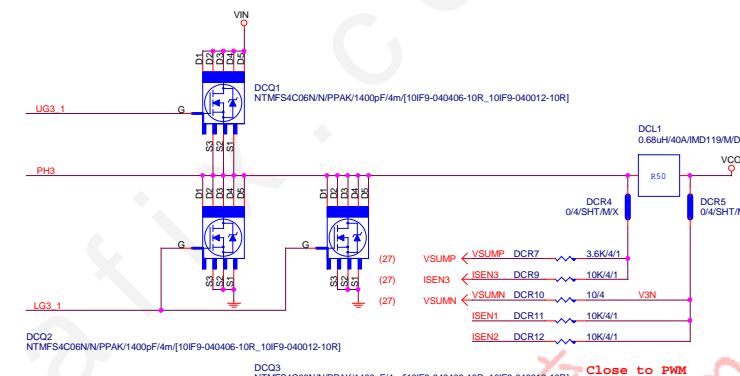
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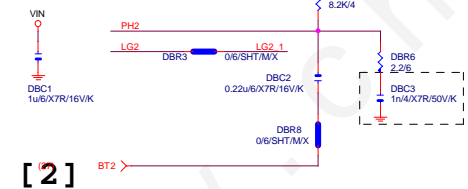
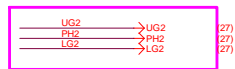
# PHASE 3



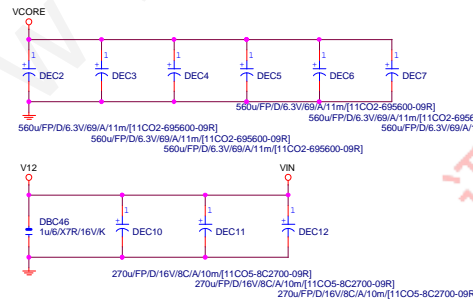
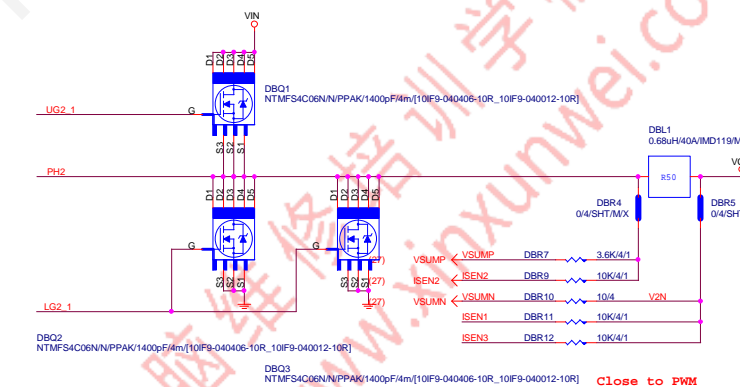
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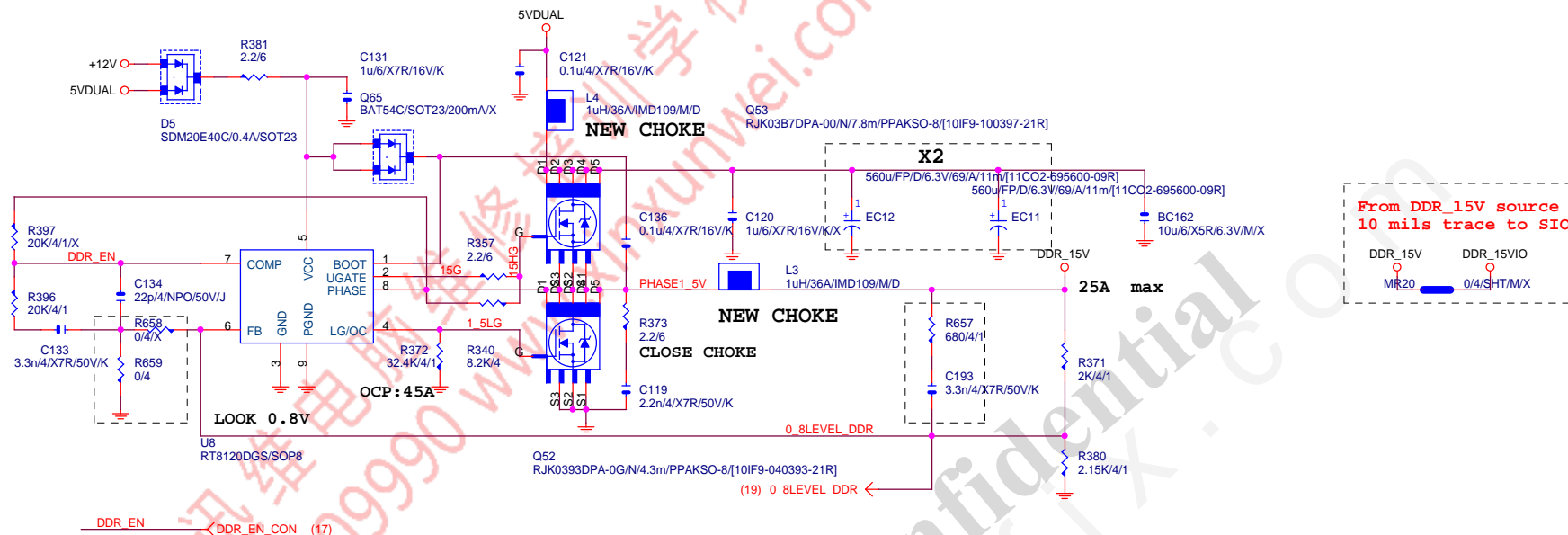
# PHASE 2



[2]







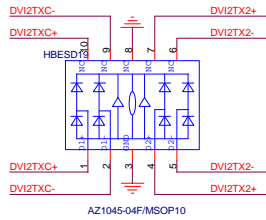
VIN=5V, VOUT=1.5V, IOU=25A, PHASE=1  
IRMS=11.45A  
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A  
Coefficient=1.7(85°C), 1(105°C)  
VIN Ripple current=4.7X1.7=7.99A(85°C)  
-->故固态电容须2X7.99=15.98>11.45A

$Rocset = (Iocp * Lgate, rdson) / Iocset$   
 $Rocset = (45A * 6.7mOhm) / 10uA = 30K$   
 $Iocset = 10uA$

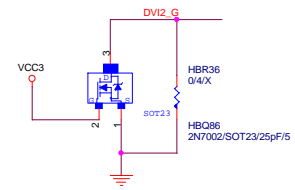
Gigabyte Technology

Title			
DDR POWER			
Size	Document Number	Rev	
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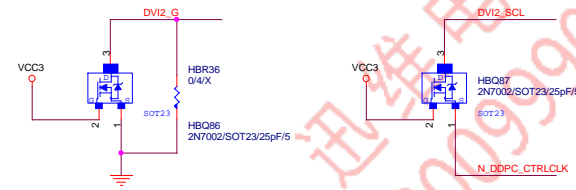
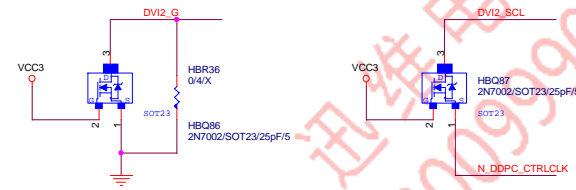
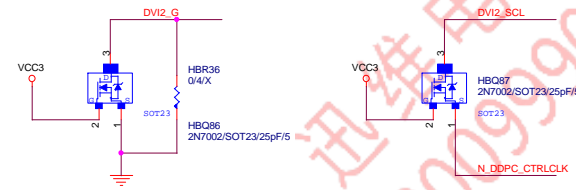
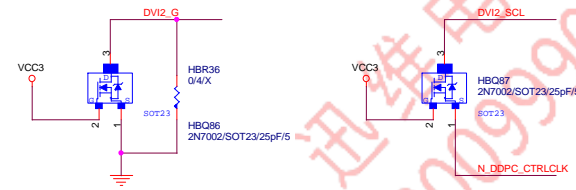
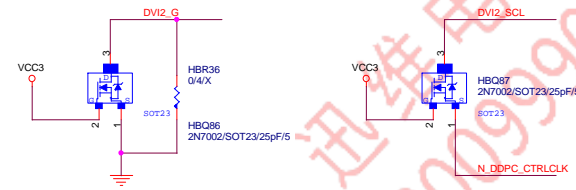
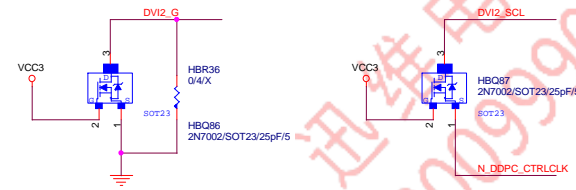
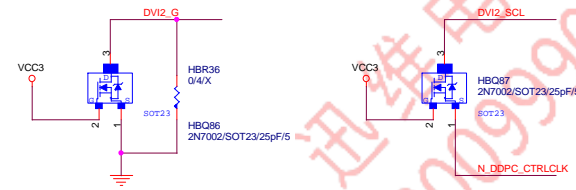
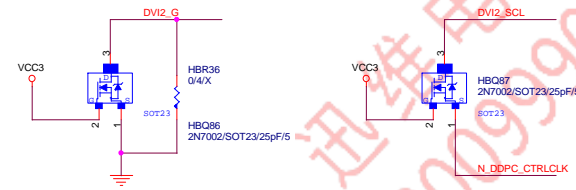
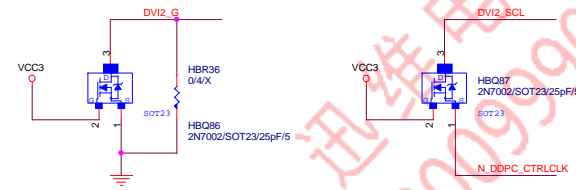
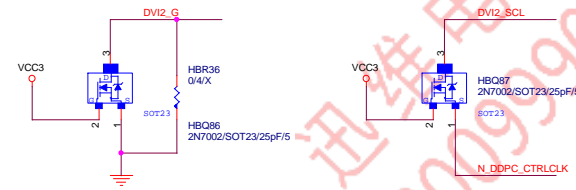
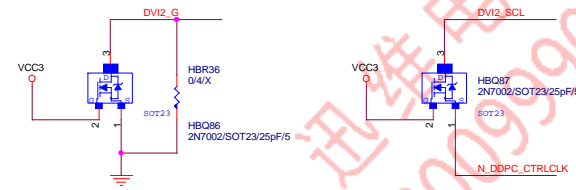
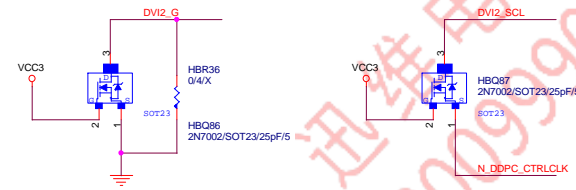
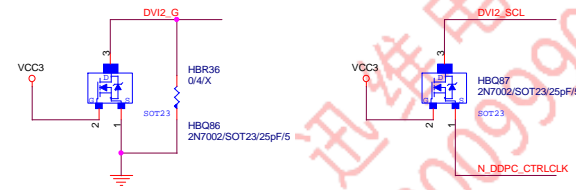
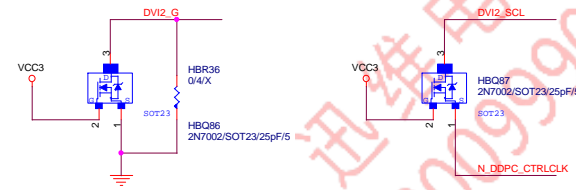
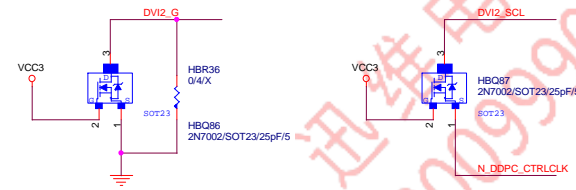
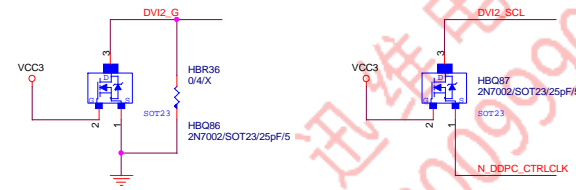
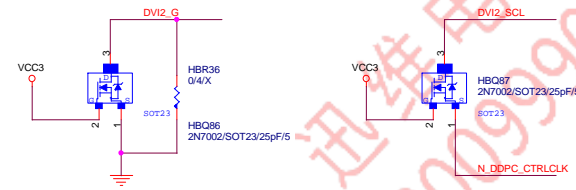
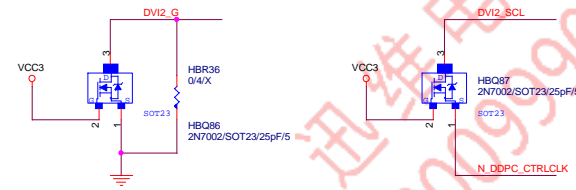
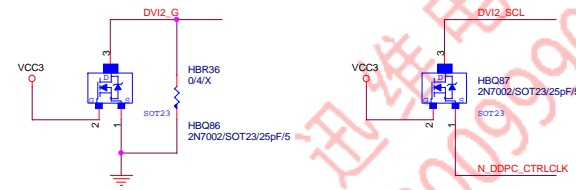
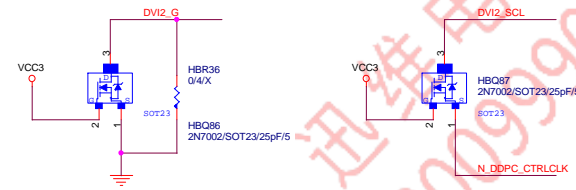
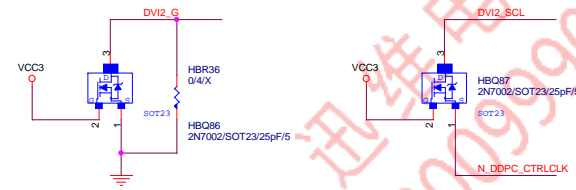
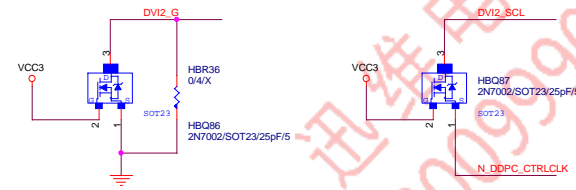
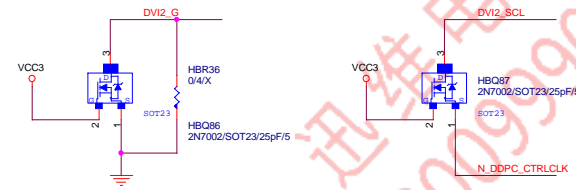
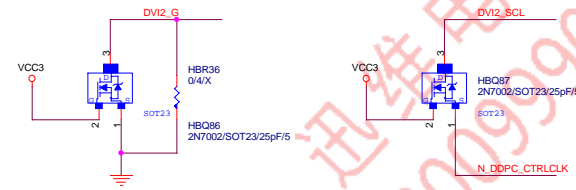
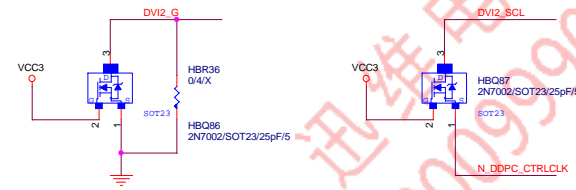
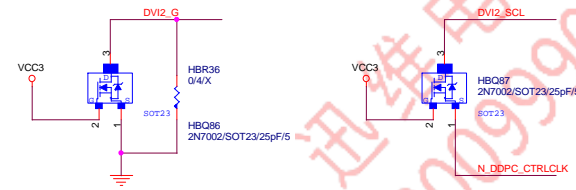
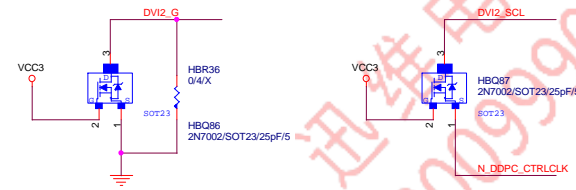
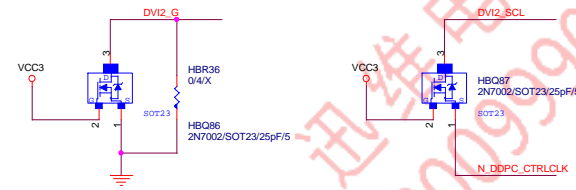
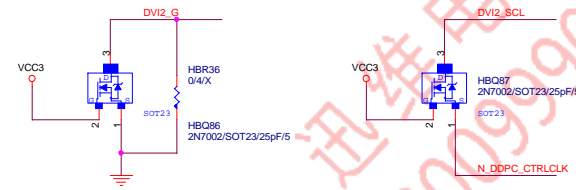
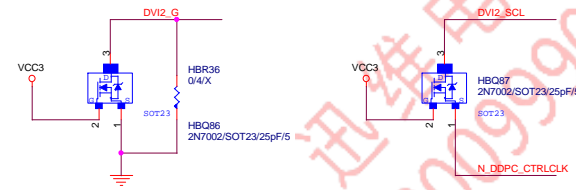
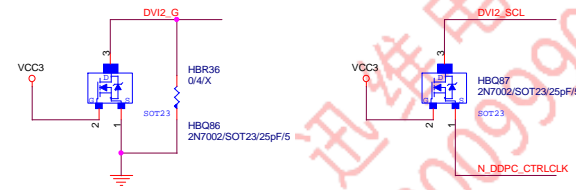
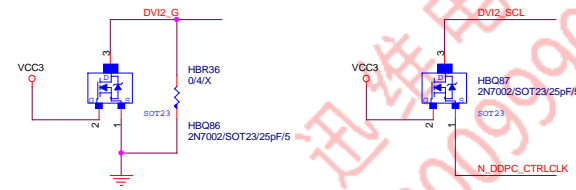
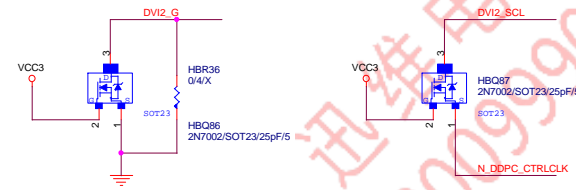
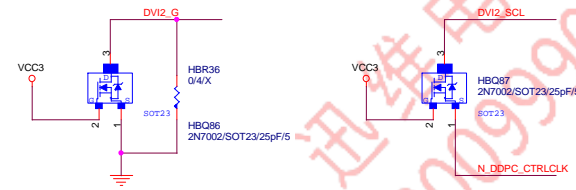
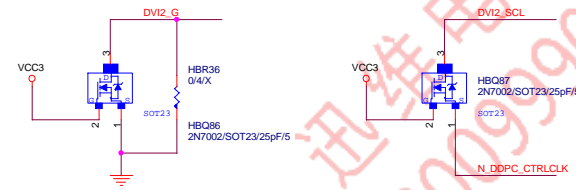
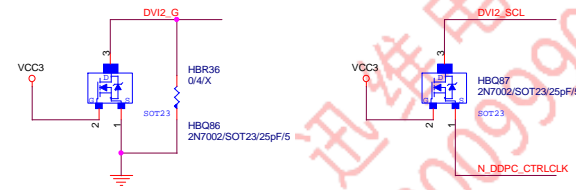
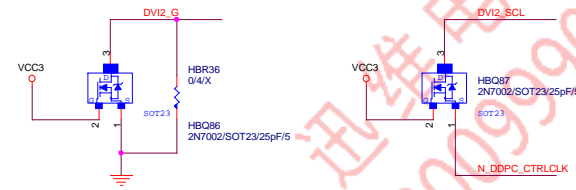
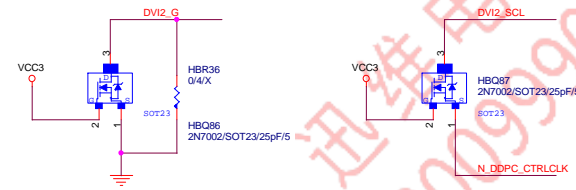
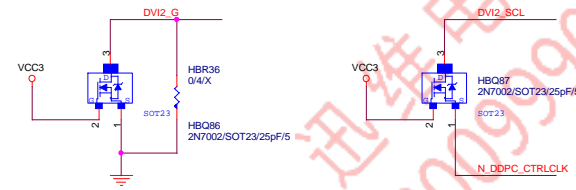
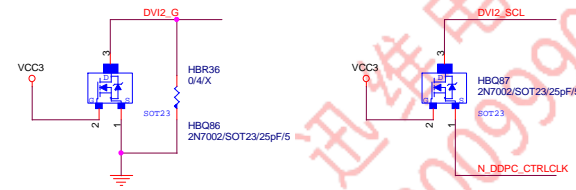
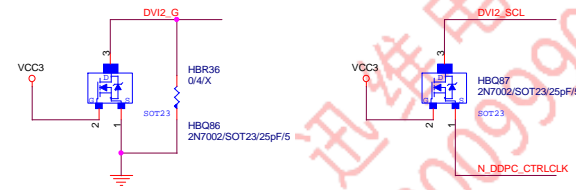
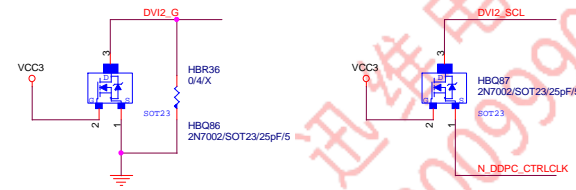
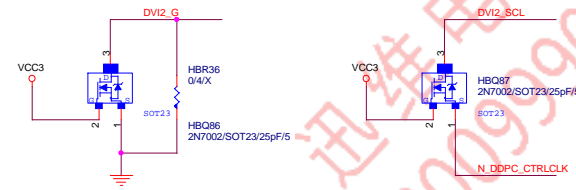
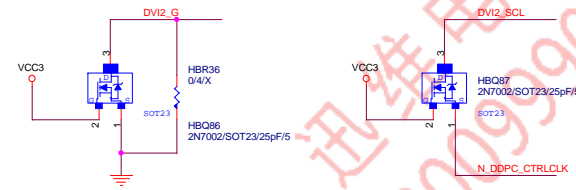
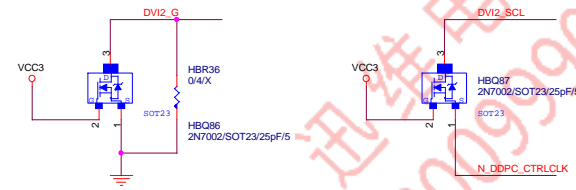
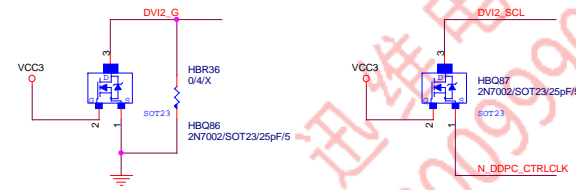
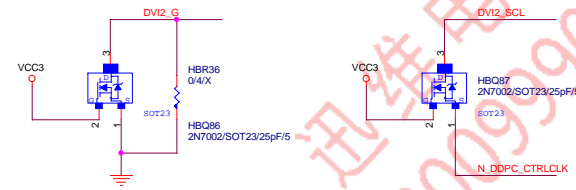
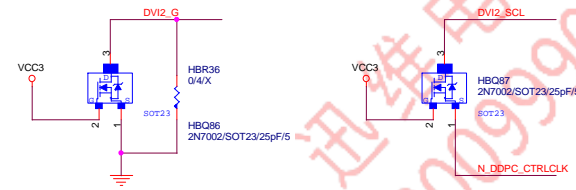
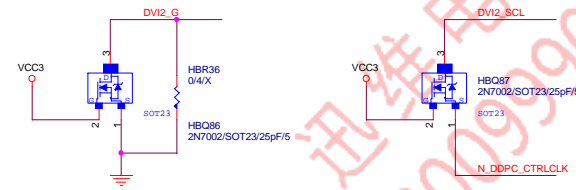
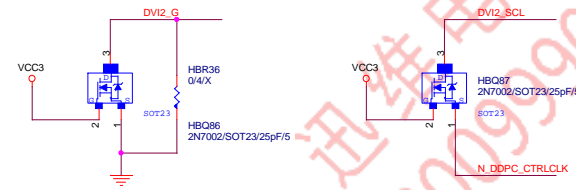
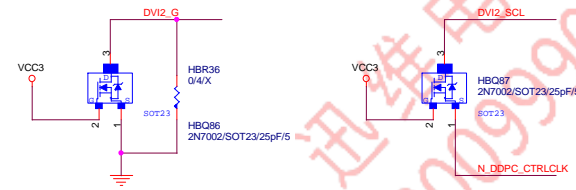
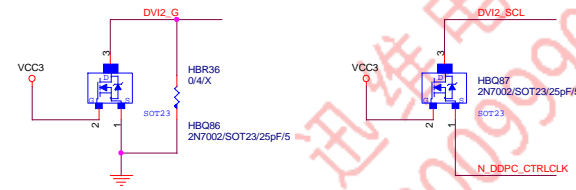
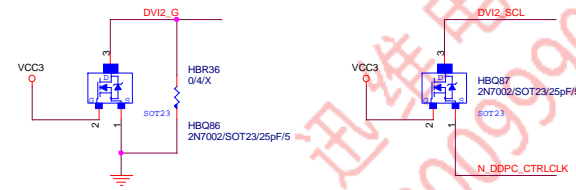
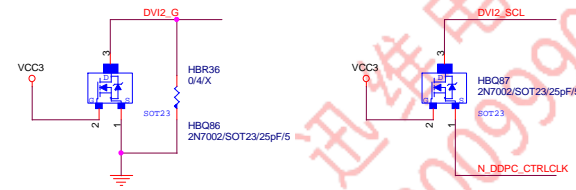
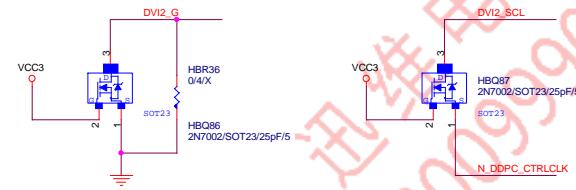
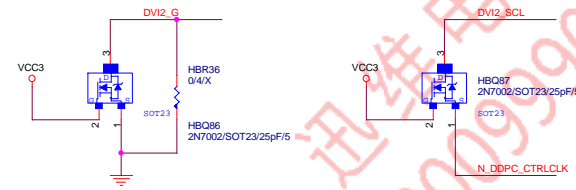
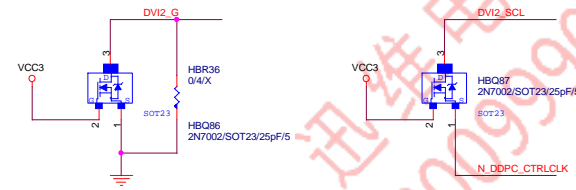
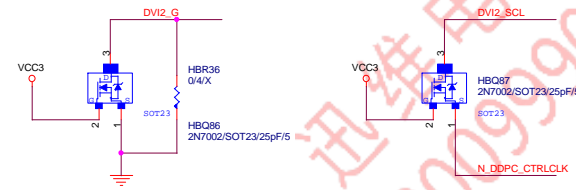
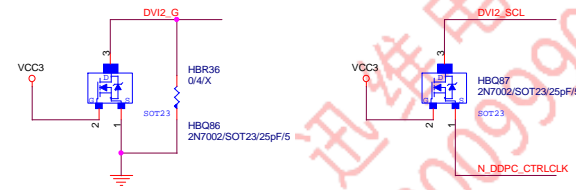
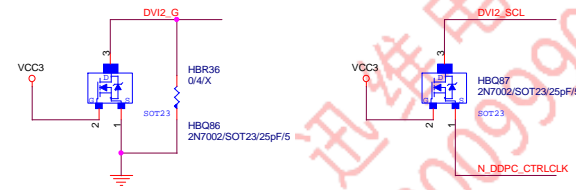
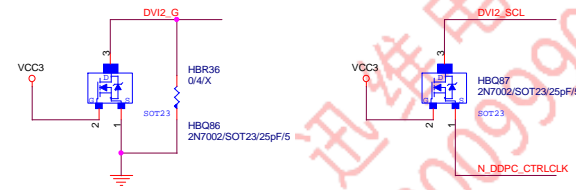
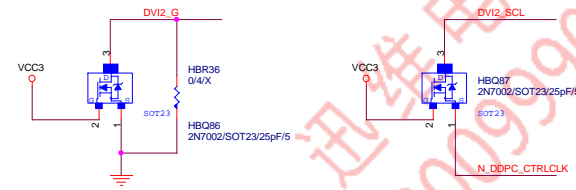
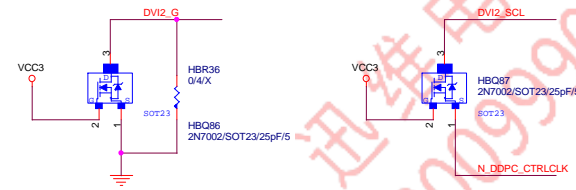
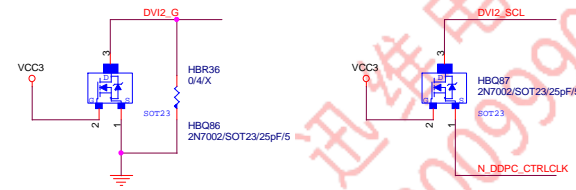
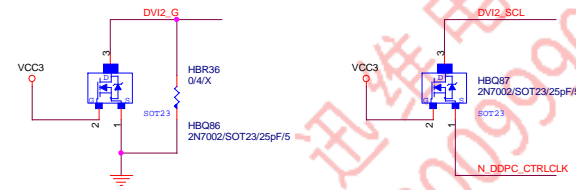
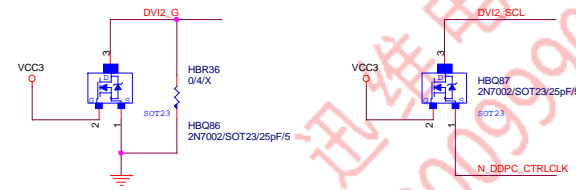
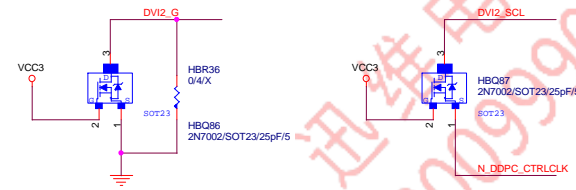
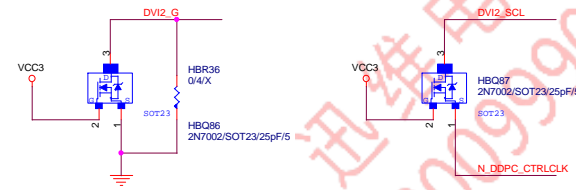
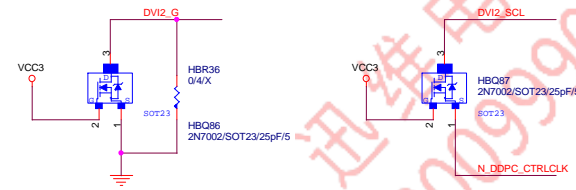
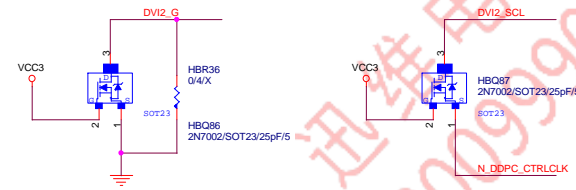
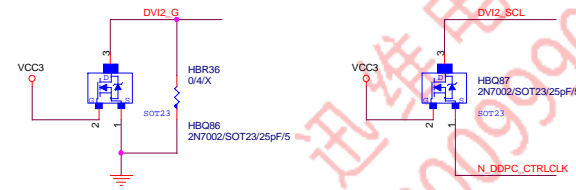
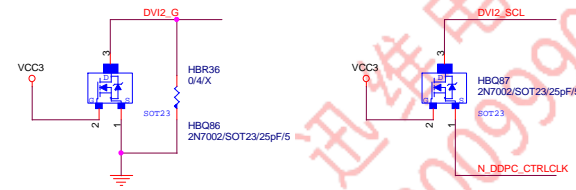
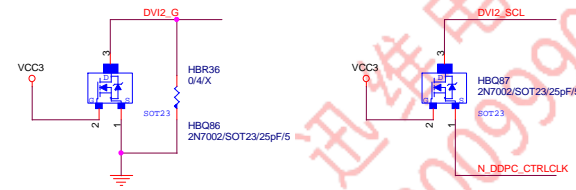
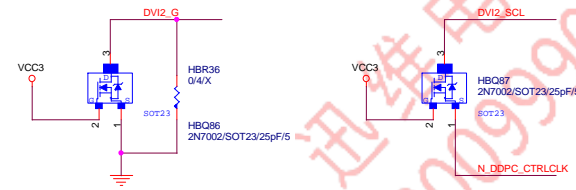
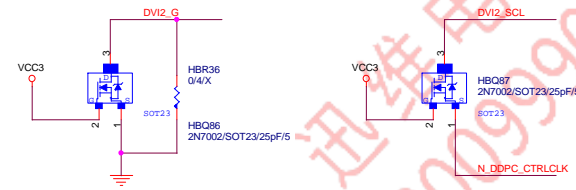
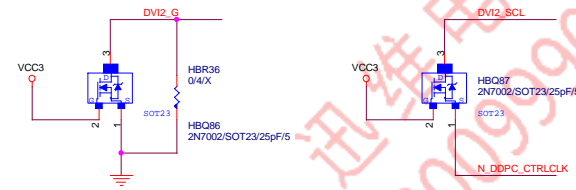
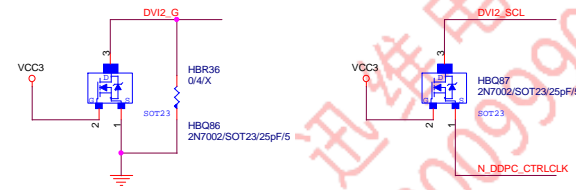
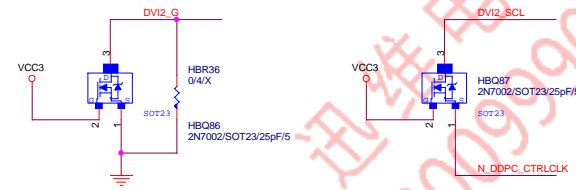
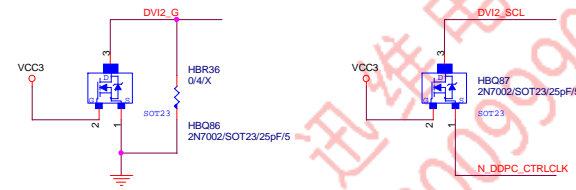
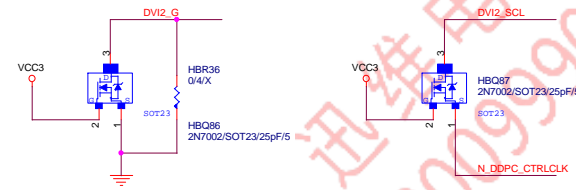
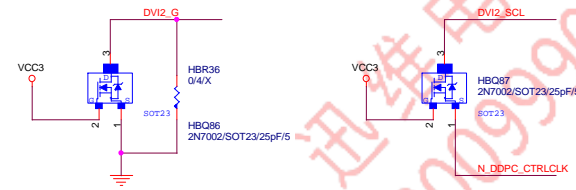
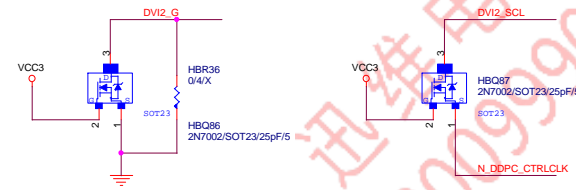
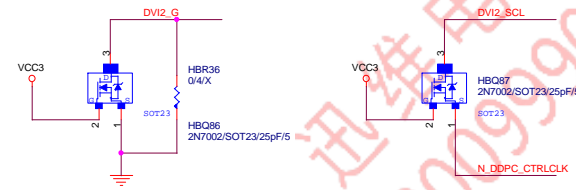
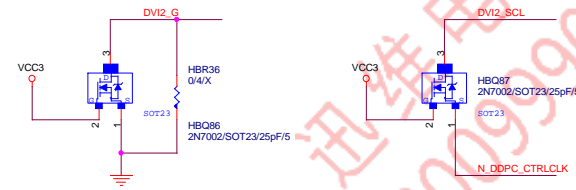
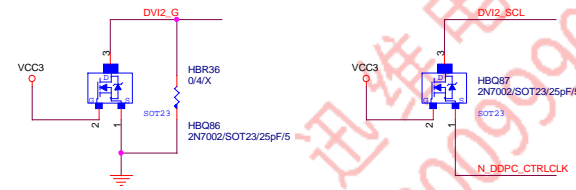
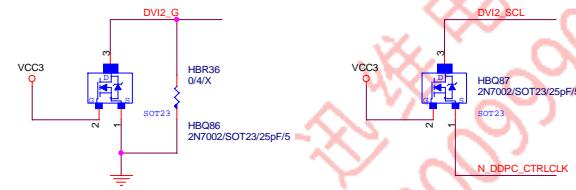
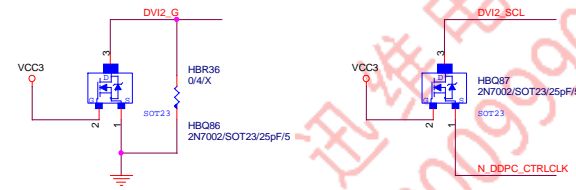
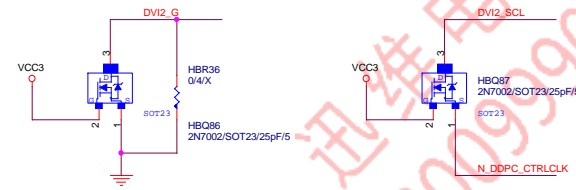
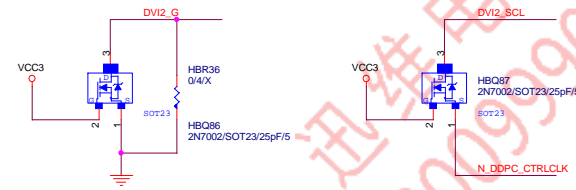
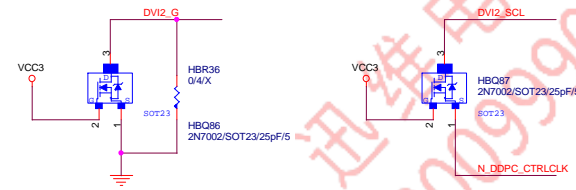
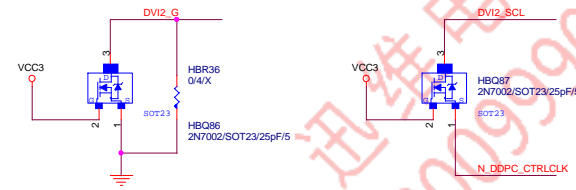
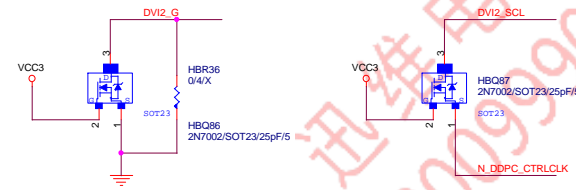
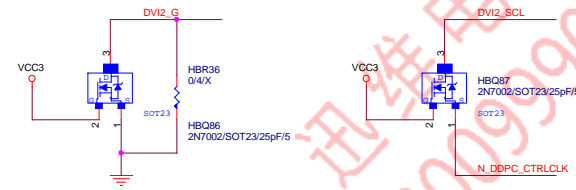
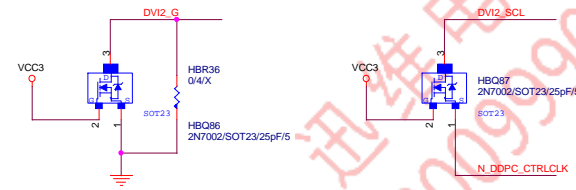
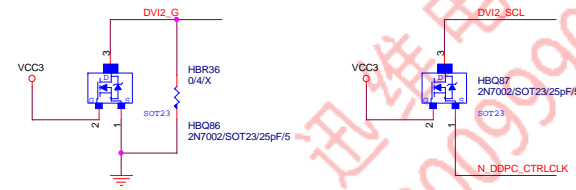
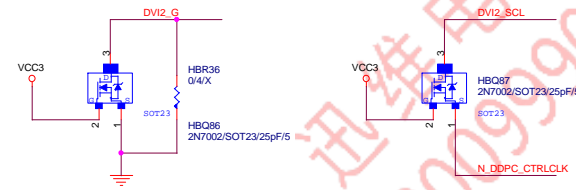
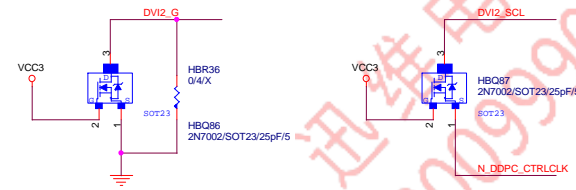
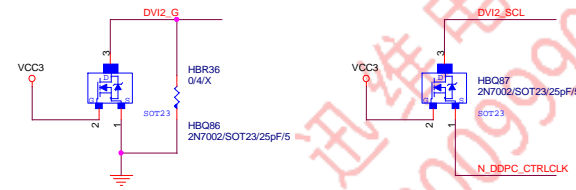
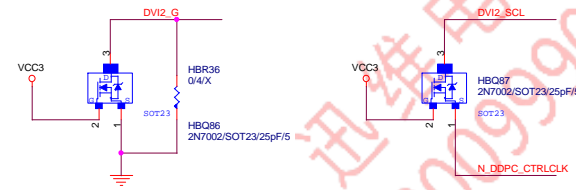
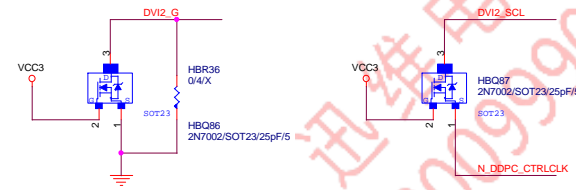
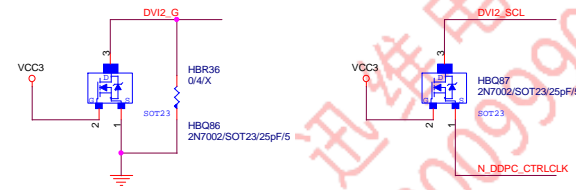
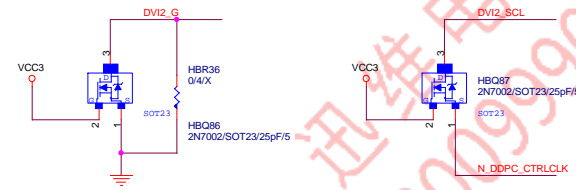
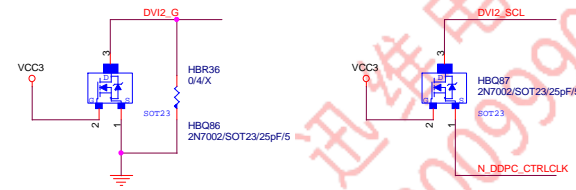
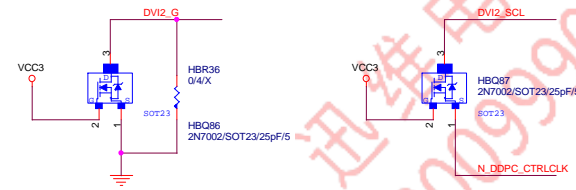
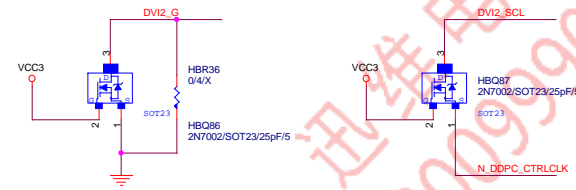
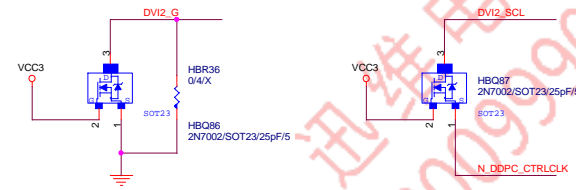
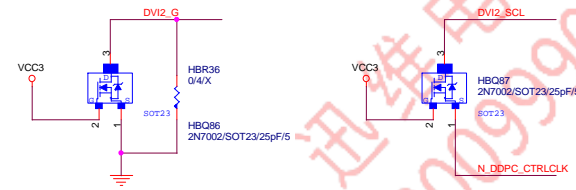
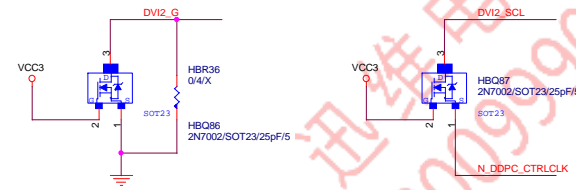
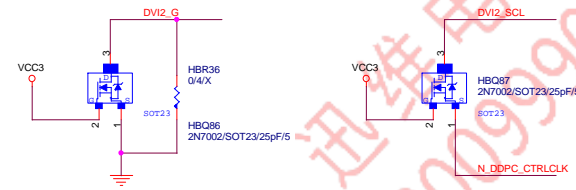
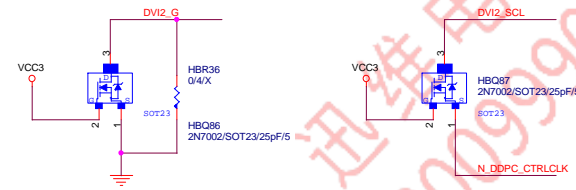
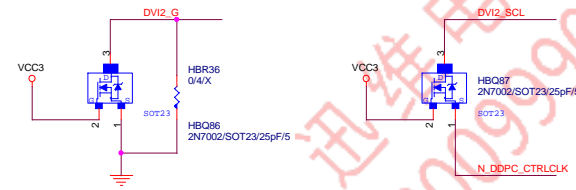
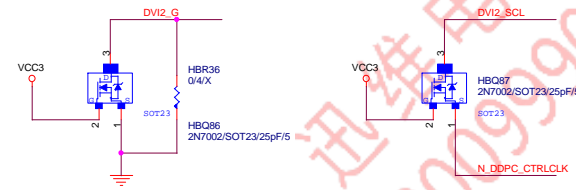
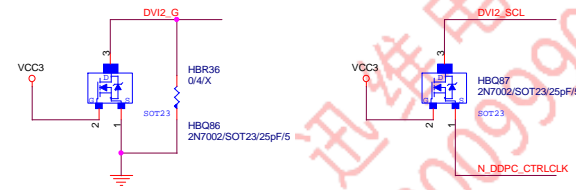
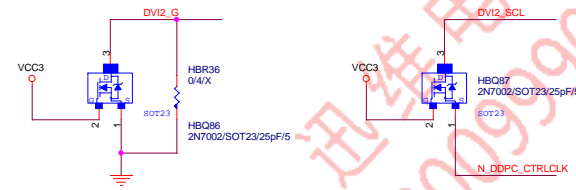
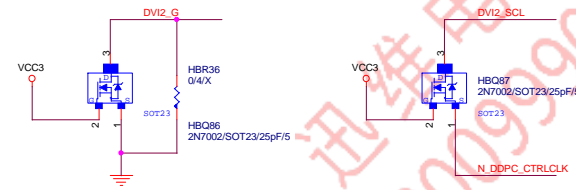
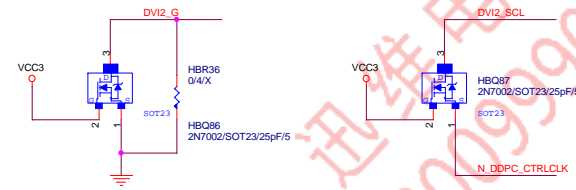
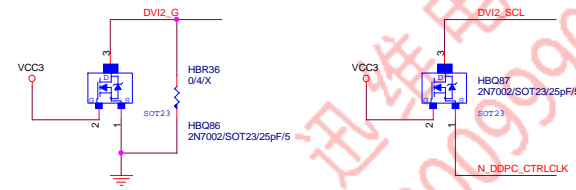




Close to connector



Close to connector





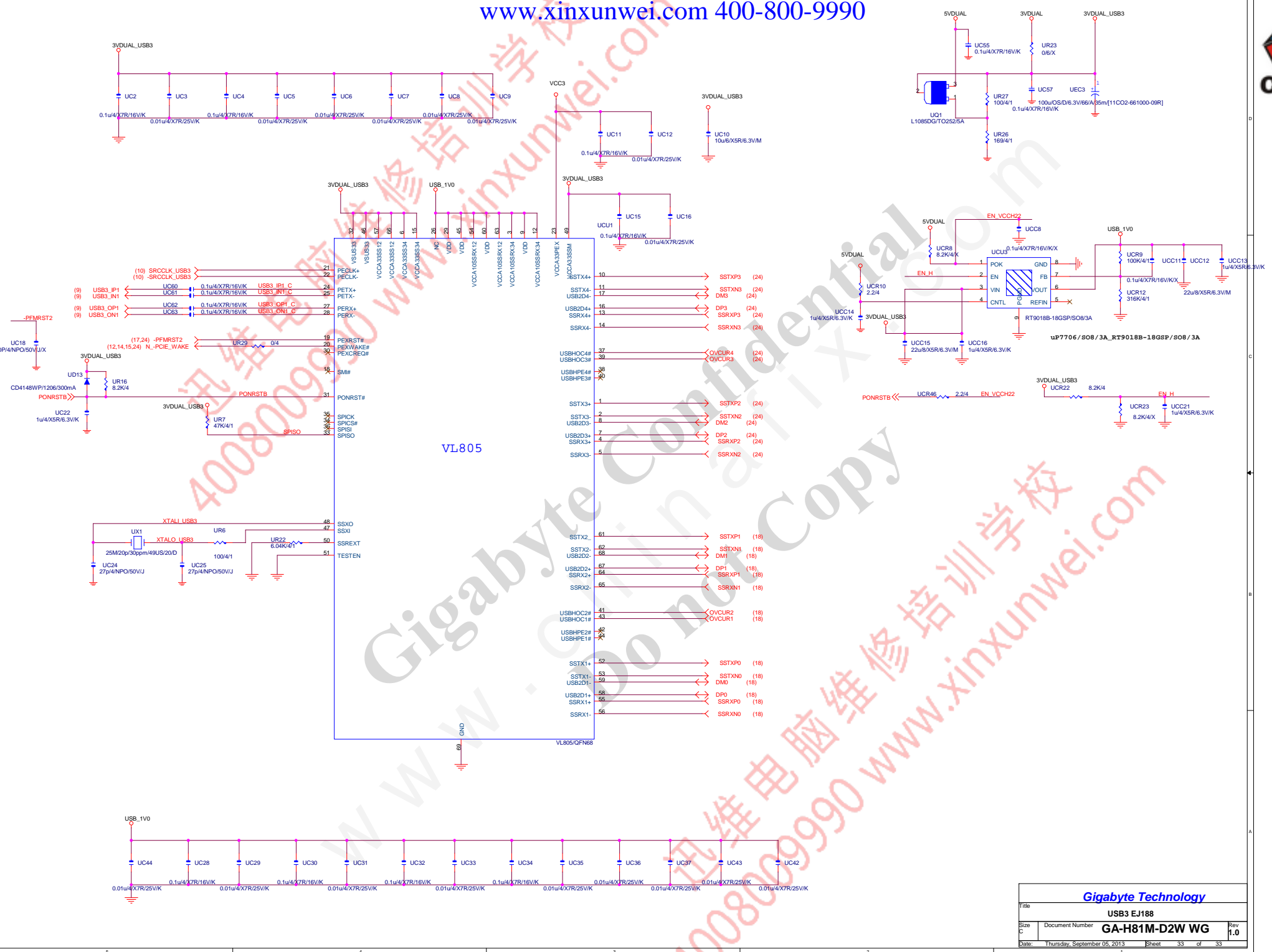
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